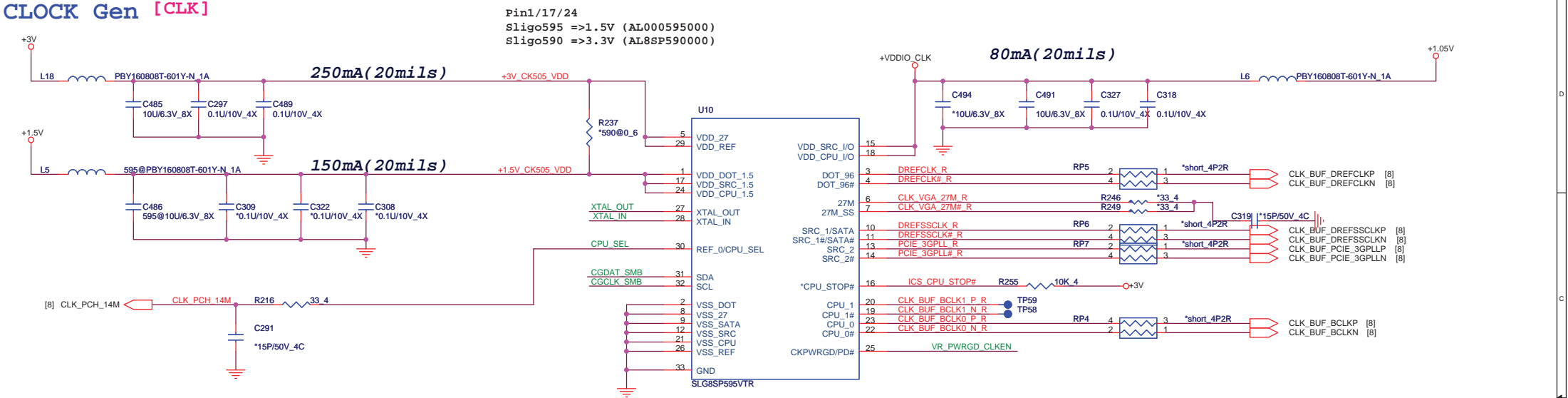
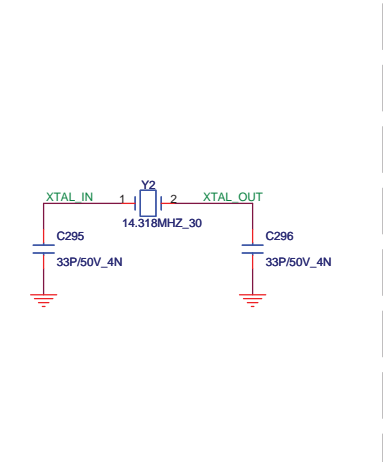


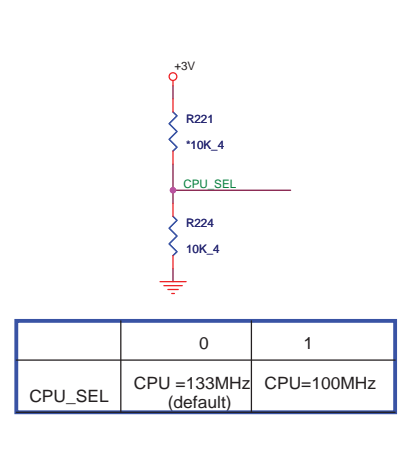
CLOCK Gen [CLK]



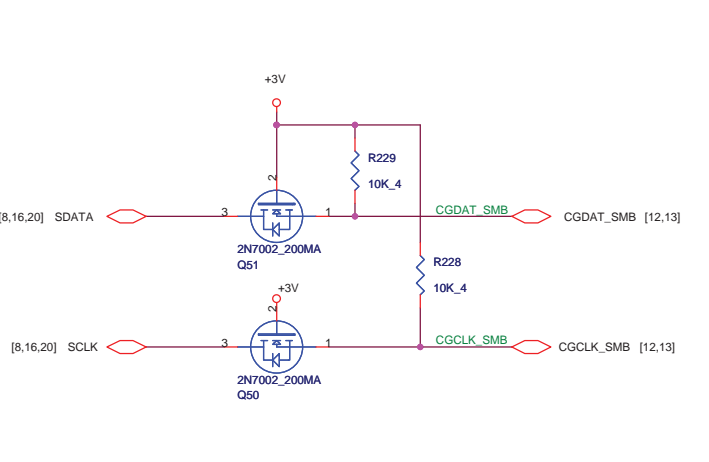
CLK CRYSTAL



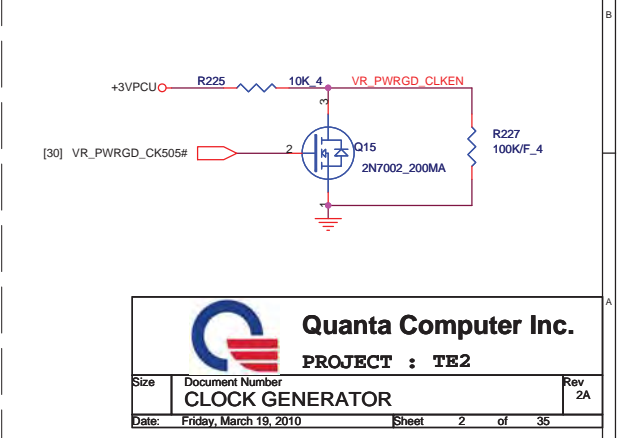
CLK CPU_SEL



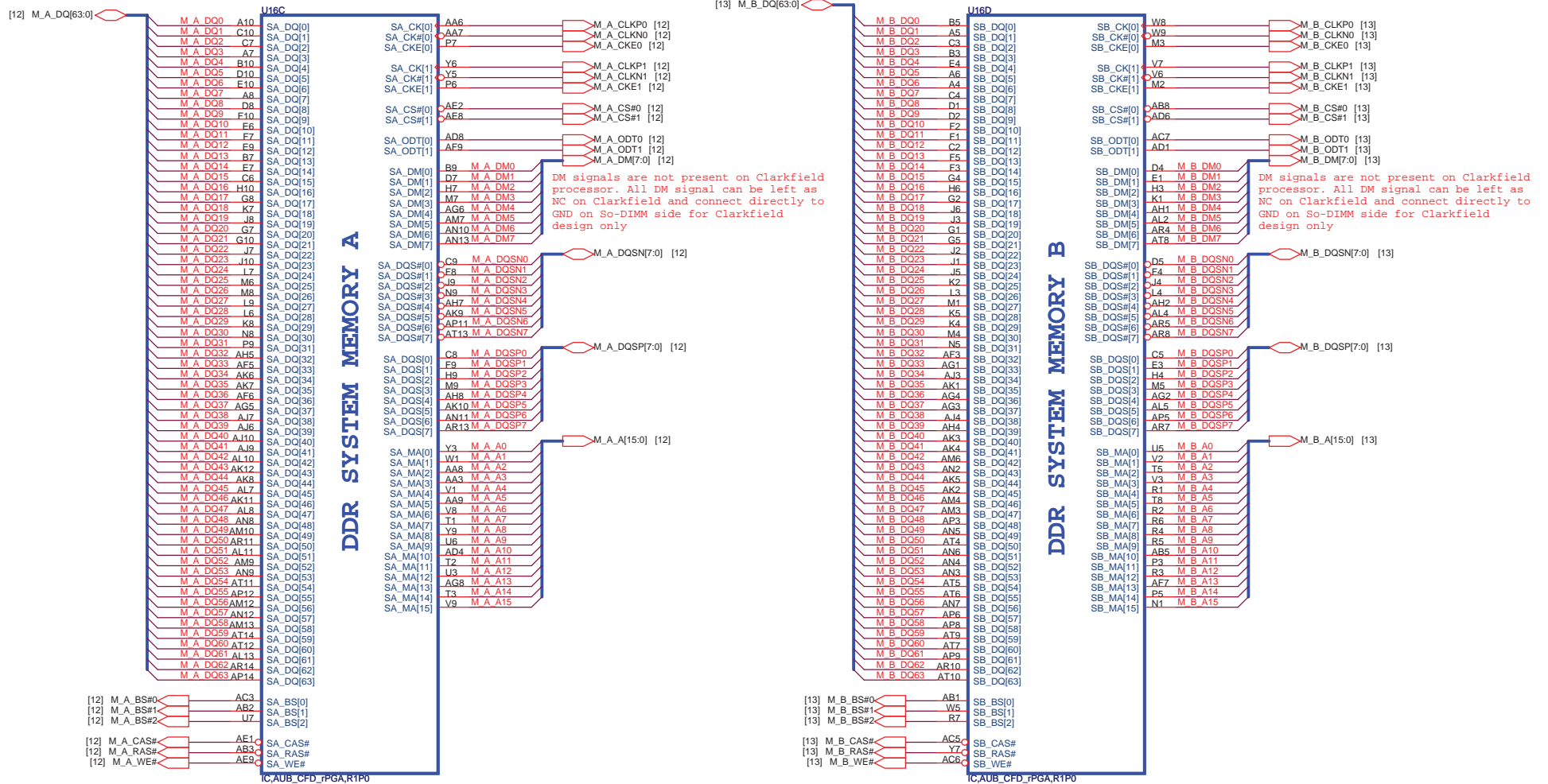
CLK I2C



CLK POWERGOOD
Change to +3VPCU
(follow CRB)

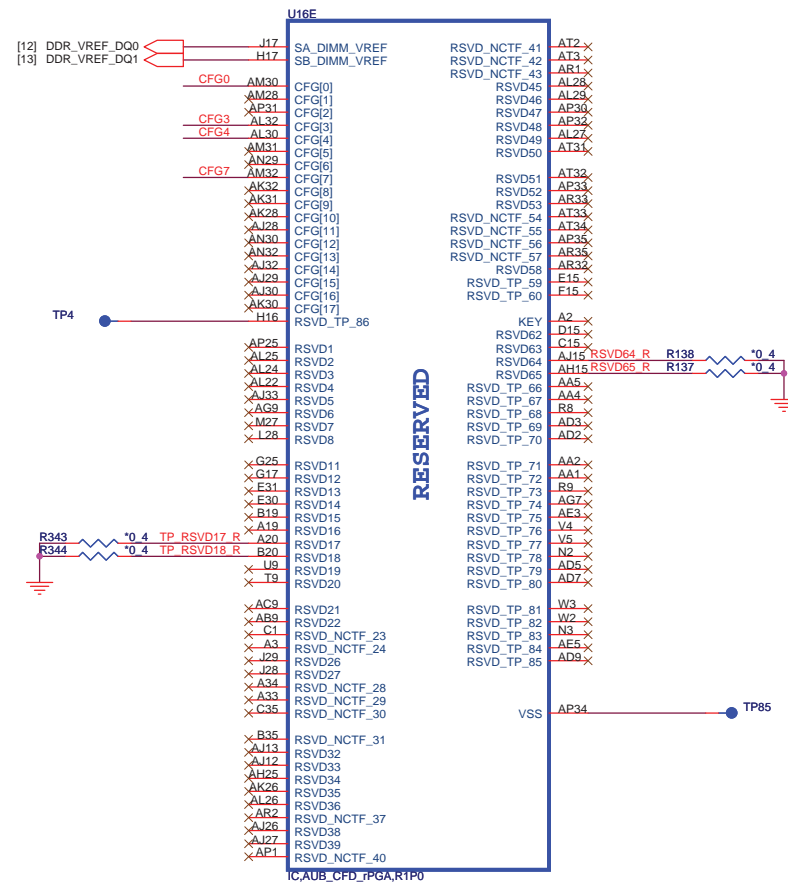
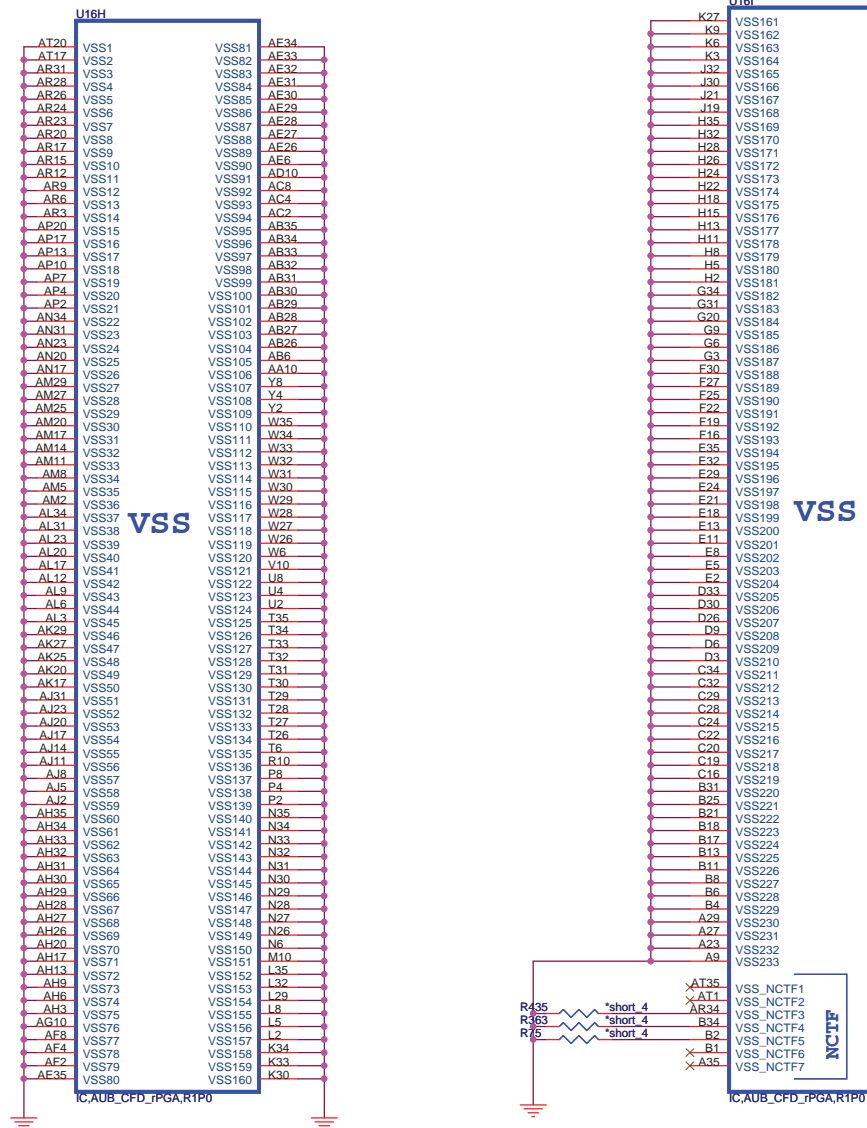


AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)



AUBURNDALE/CLARKSFIELD PROCESSOR (GND)

AUBURNDALE/CLARKSFIELD PROCESSOR(RESERVED, CFG)



For Discrete only



CFG[1:0] - PCI_Epress Configuration Select
 * 11= 1 x 16 PEG
 * 10= 2 x 8 PEG

	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed 15 -> 0, 14 -> 1

Quanta Computer Inc.
PROJECT : TE2

Size Document Number

PROCESSOR 4/4 (GND)

Date: Tuesday, March 09, 2010 Sheet 6 of 35

Rev 2A

The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.01K +/- 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.

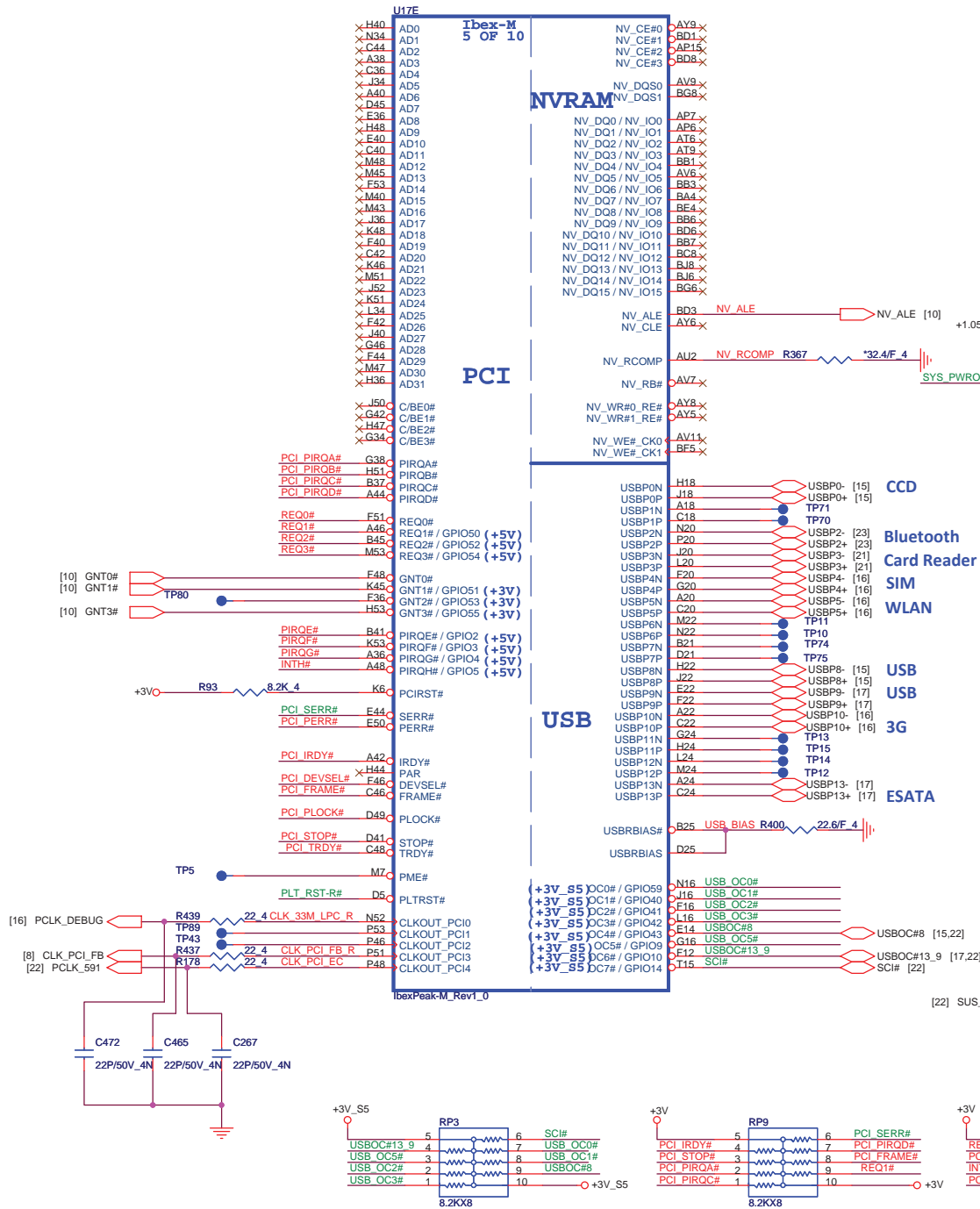
WWW.AliSaler.Com

IBEX PEAK-M (GND)

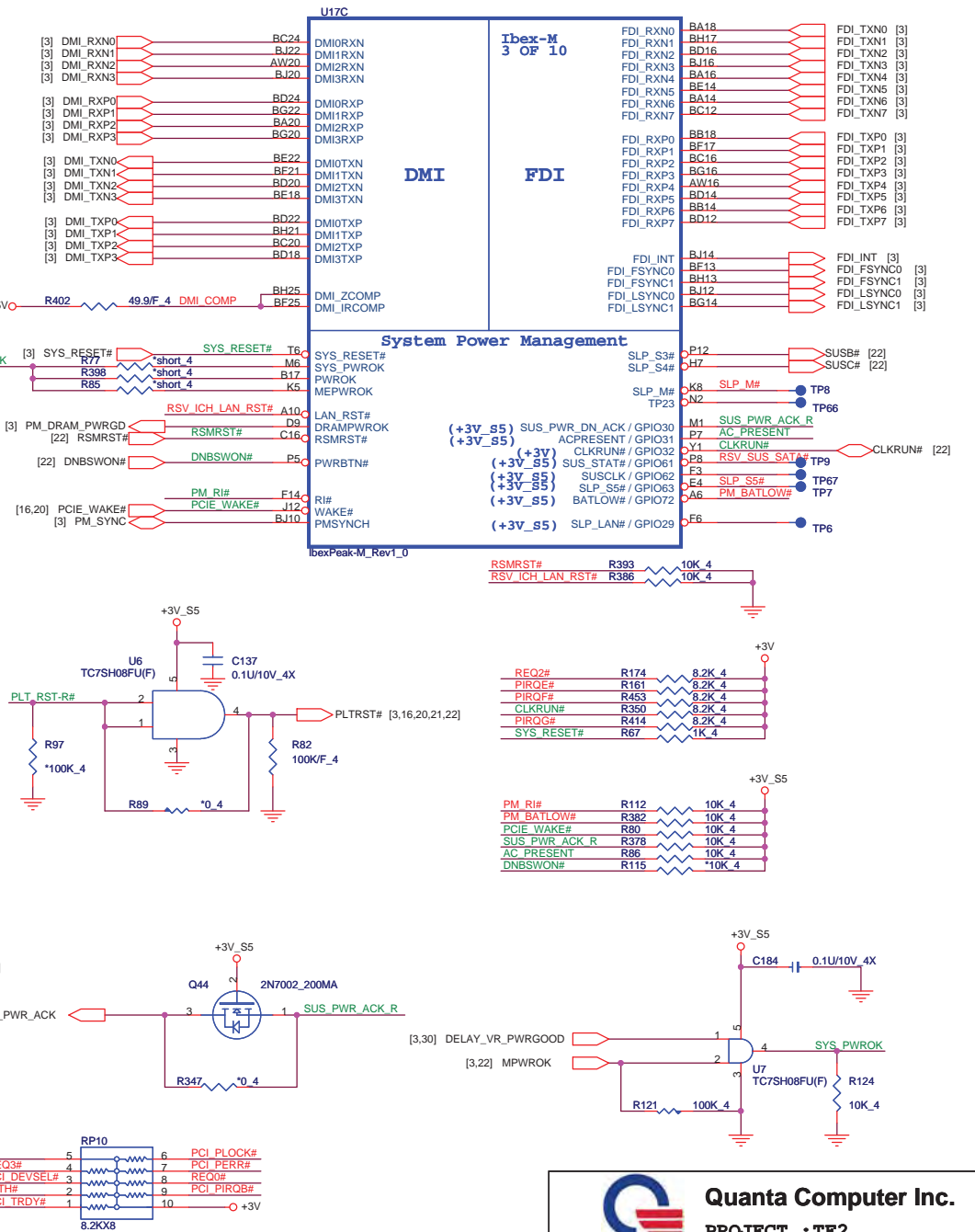
U171

AV7	VSS[159]	VSS[269]	H49
B11	VSS[160]	VSS[260]	H5
B15	VSS[161]	VSS[261]	J24
B19	VSS[162]	VSS[262]	K11
B23	VSS[163]	VSS[263]	K43
B31	VSS[164]	VSS[264]	K47
B39	VSS[165]	VSS[265]	L14
B43	VSS[166]	VSS[266]	L18
B47	VSS[167]	VSS[267]	L22
B7	VSS[168]	VSS[268]	L22
B12	VSS[169]	VSS[269]	L32
B12	VSS[170]	VSS[270]	L36
B16	VSS[171]	VSS[271]	L40
B20	VSS[172]	VSS[272]	L52
B24	VSS[173]	VSS[273]	M12
B24	VSS[174]	VSS[274]	M16
B30	VSS[175]	VSS[275]	M20
B34	VSS[176]	VSS[276]	M38
B38	VSS[177]	VSS[277]	M42
B42	VSS[178]	VSS[278]	M46
B46	VSS[179]	VSS[279]	M49
B50	VSS[180]	VSS[280]	M5
BC10	VSS[181]	VSS[281]	N24
BC14	VSS[182]	VSS[282]	P11
BC18	VSS[183]	VSS[283]	P15
BC2	VSS[184]	VSS[284]	P22
BC22	VSS[185]	VSS[285]	P32
BC32	VSS[186]	VSS[286]	P34
BC36	VSS[187]	VSS[287]	P42
BC40	VSS[188]	VSS[288]	P45
BC44	VSS[189]	VSS[289]	P47
BC52	VSS[190]	VSS[290]	R2
BH9	VSS[191]	VSS[291]	R52
BD48	VSS[192]	VSS[292]	T12
BD49	VSS[193]	VSS[293]	T41
BD5	VSS[194]	VSS[294]	T46
BE12	VSS[195]	VSS[295]	T49
BE16	VSS[196]	VSS[296]	T5
BE20	VSS[197]	VSS[297]	T8
BE24	VSS[198]	VSS[298]	T10
BE30	VSS[199]	VSS[299]	T12
BE34	VSS[200]	VSS[300]	T13
BE38	VSS[201]	VSS[301]	T14
BE42	VSS[202]	VSS[302]	T15
BE46	VSS[203]	VSS[303]	T16
BE50	VSS[204]	VSS[304]	T17
BE54	VSS[205]	VSS[305]	T18
BE58	VSS[206]	VSS[306]	T19
BF3	VSS[207]	VSS[307]	T20
BF9	VSS[208]	VSS[308]	T21
BF12	VSS[209]	VSS[309]	T22
BF16	VSS[210]	VSS[310]	T23
BF20	VSS[211]	VSS[311]	T24
BF24	VSS[212]	VSS[312]	T25
BF28	VSS[213]	VSS[313]	T26
BF32	VSS[214]	VSS[314]	T27
BF36	VSS[215]	VSS[315]	T28
BF40	VSS[216]	VSS[316]	T29
BF44	VSS[217]	VSS[317]	T30
BF48	VSS[218]	VSS[318]	T31
BF52	VSS[219]	VSS[319]	T32
BF56	VSS[220]	VSS[320]	T33
BF60	VSS[221]	VSS[321]	T34
BF64	VSS[222]	VSS[322]	T35
BF68	VSS[223]	VSS[323]	T36
BF72	VSS[224]	VSS[324]	T37
BF76	VSS[225]	VSS[325]	T38
BF80	VSS[226]	VSS[326]	T39
BF84	VSS[227]	VSS[327]	T40
BF88	VSS[228]	VSS[328]	T41
BF92	VSS[229]	VSS[329]	T42
BF96	VSS[230]	VSS[330]	T43
BF100	VSS[231]	VSS[331]	T44
BF104	VSS[232]	VSS[332]	T45
BF108	VSS[233]	VSS[333]	T46
BF112	VSS[234]	VSS[334]	T47
BF116	VSS[235]	VSS[335]	T48
BF120	VSS[236]	VSS[336]	T49
BF124	VSS[237]	VSS[337]	T50
BF128	VSS[238]	VSS[338]	T51
BF132	VSS[239]	VSS[339]	T52
BF136	VSS[240]	VSS[340]	T53
BF140	VSS[241]	VSS[341]	T54
BF144	VSS[242]	VSS[342]	T55
BF148	VSS[243]	VSS[343]	T56
BF152	VSS[244]	VSS[344]	T57
BF156	VSS[245]	VSS[345]	T58
BF160	VSS[246]	VSS[346]	T59
BF164	VSS[247]	VSS[347]	T60
BF168	VSS[248]	VSS[348]	T61
BF172	VSS[249]	VSS[349]	T62
BF176	VSS[250]	VSS[350]	T63
BF180	VSS[251]	VSS[351]	T64
BF184	VSS[252]	VSS[352]	T65
BF188	VSS[253]	VSS[353]	T66
BF192	VSS[254]	VSS[354]	T67
BF196	VSS[255]	VSS[355]	T68
BF200	VSS[256]	VSS[356]	T69
BF204	VSS[257]	VSS[357]	T70
BF208	VSS[258]	VSS[358]	T71
BF212	VSS[259]	VSS[359]	T72
BF216	VSS[260]	VSS[360]	T73
BF220	VSS[261]	VSS[361]	T74
BF224	VSS[262]	VSS[362]	T75
BF228	VSS[263]	VSS[363]	T76
BF232	VSS[264]	VSS[364]	T77
BF236	VSS[265]	VSS[365]	T78
BF240	VSS[266]	VSS[366]	T79
BF244	VSS[267]	VSS[367]	T80
BF248	VSS[268]	VSS[368]	T81
BF252	VSS[269]	VSS[369]	T82
BF256	VSS[270]	VSS[370]	T83
BF260	VSS[271]	VSS[371]	T84
BF264	VSS[272]	VSS[372]	T85
BF268	VSS[273]	VSS[373]	T86
BF272	VSS[274]	VSS[374]	T87
BF276	VSS[275]	VSS[375]	T88
BF280	VSS[276]	VSS[376]	T89
BF284	VSS[277]	VSS[377]	T90
BF288	VSS[278]	VSS[378]	T91
BF292	VSS[279]	VSS[379]	T92
BF296	VSS[280]	VSS[380]	T93
BF300	VSS[281]	VSS[381]	T94
BF304	VSS[282]	VSS[382]	T95
BF308	VSS[283]	VSS[383]	T96
BF312	VSS[284]	VSS[384]	T97
BF316	VSS[285]	VSS[385]	T98
BF320	VSS[286]	VSS[386]	T99
BF324	VSS[287]	VSS[387]	T100
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BF352	VSS[294]	VSS[394]	T107
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BF368	VSS[298]	VSS[398]	T111
BF372	VSS[299]	VSS[399]	T112
BF376	VSS[300]	VSS[400]	T113
BF380	VSS[301]	VSS[401]	T114
BF384	VSS[302]	VSS[402]	T115
BF388	VSS[303]	VSS[403]	T116
BF392	VSS[304]	VSS[404]	T117
BF396	VSS[305]	VSS[405]	T118
BF400	VSS[306]	VSS[406]	T119
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BF412	VSS[309]	VSS[409]	T122
BF416	VSS[310]	VSS[410]	T123
BF420	VSS[311]	VSS[411]	T124
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BF608	VSS[358]	VSS[458]	T171
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BF624	VSS[362]	VSS[462]	T175
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BF640	VSS[366]	VSS[466]	T179
BF644	VSS[367]	VSS[467]	T180
BF648	VSS[368]	VSS[468]	T181
BF652	VSS[369]	VSS[469]	T182
BF656	VSS[370]	VSS[470]	T183
BF660	VSS[371]	VSS[471]	T184
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BF672	VSS[374]	VSS[474]	T187
BF676	VSS[375]	VSS[475]	T188
BF680	VSS[376]	VSS[476]	T189
BF684	VSS[377]	VSS[477]	T190
BF688	VSS[378]	VSS[478]	T191
BF692	VSS[379]	VSS[479]	T192
BF696	VSS[380]	VSS[480]	T193
BF700	VSS[381]	VSS[481]	T194
BF704	VSS[382]	VSS[482]	T195
BF708	VSS[383]	VSS[483]	T196
BF712	VSS[384]	VSS[484]	T197
BF716	VSS[385]	VSS[485]	T198
BF720	VSS[386]	VSS[486]	T199
BF724	VSS[387]	VSS[487]	T200
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BF760	VSS[396]	VSS[496]	T209
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BF784	VSS[402]	VSS[502]	T215
BF788	VSS[403]	VSS[503]	T216
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BF796	VSS[405]	VSS[505]	T218
BF800	VSS[406]	VSS[506]	T219
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BF808	VSS[408]	VSS[508]	T221
BF812	VSS[409]	VSS[509]	T222
BF816	VSS[410]	VSS[510]	T223
BF820	VSS[411]	VSS[511]	T224
BF824	VSS[412]	VSS[512]	T225
BF828	VSS[413]	VSS[513]	T226
BF832	VSS[414]	VSS[514]	T227
BF836	VSS[415]	VSS[515]	T228
BF840	VSS[416]	VSS[516]	T229
BF844	VSS[417]	VSS[517]	T230
BF848	VSS[418]	VSS[518]	T231
BF852	VSS[419]	VSS[519]	T232
BF856	VSS[420]	VSS[520]	T233
BF860	VSS[421]	VSS[521]	T234
BF864	VSS[422]	VSS[522]	T235
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BF872	VSS[424]	VSS[524]	T237
BF876	VSS[425]	VSS[525]	T238
BF880	VSS[426]	VSS[526]	T239
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BF896	VSS[430]	VSS[530]	T243
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BF904	VSS[432]	VSS[532]	T245
BF908	VSS[433]	VSS[533]	T246
BF912	VSS[434]	VSS[534]	T247
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BF920	VSS[436]	VSS[536]	T249
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BF956	VSS[445]	VSS[545]	T258
BF960	VSS[446]	VSS[546]	T259
BF964	VSS[447]	VSS[547]	T260
BF968	VSS[448]	VSS[548]	T261
BF972	VSS[449]	VSS[549]	T262
BF976	VSS[450]	VSS[550]	T263
BF980	VSS[451]	VSS[551]	T264
BF984	VSS[452]	VSS[552]	T265
BF988	VSS[453]	VSS[553]	T266
BF992	VSS[454]	VSS[554]	T267
BF996	VSS[455]	VSS[555]	T268
BF1000	VSS[456]	VSS[556]	T269
BF1004	VSS[

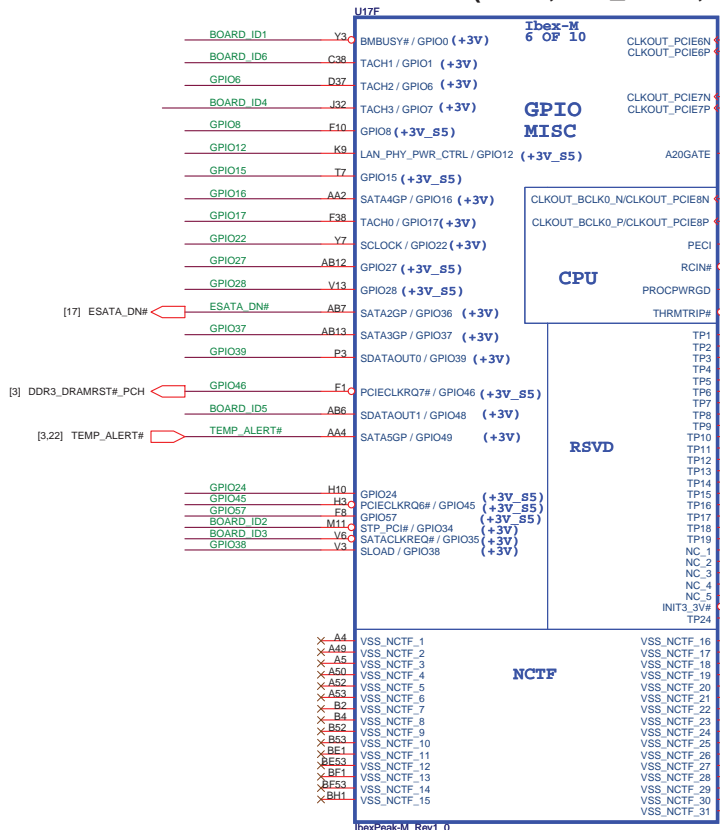
IBEX PEAK-M (PCI,USB,NVRAM)



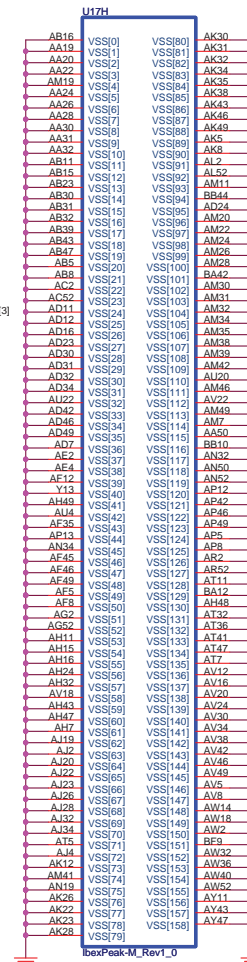
IBEX PEAK-M (DMI,FDI,GPIO)



IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)



IBEX PEAK-M (GND)



PCH Strap Pin Configuration Table

SPKR



0 = Default Mode (Internal weak Pull-down)
1 = No Reboot Mode with TCO Disabled

GNT3#/
GPIO55

0 = Default Mode (Internal weak Pull-down)
1 = No Reboot Mode with TCO Disabled

HDA_DOCK_EN
#GPIO33

0 = Top Block Swap Mode
1 = Default Mode (Internal pull-up)

GNT0#,
GNT1#

Boot BIOS Strap

PCI_GNT0#	GNT#1	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

SPI_MOSI



NV_ALE



1 = Enabled
0 = Disabled (Default)

GPIO8



This signal has a weak internal pull up.
NOTE: This signal should not be pulled low

GPIO15



0 = Intel ME Crypto Transport Layer Security (TLS) cipher suite with no confidentiality
1 = Intel ME Crypto Transport Layer Security (TLS) cipher suite with confidentiality

GPIO27



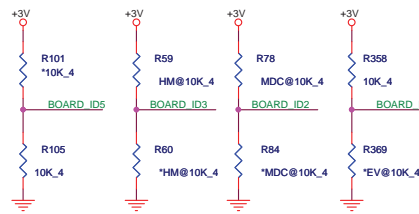
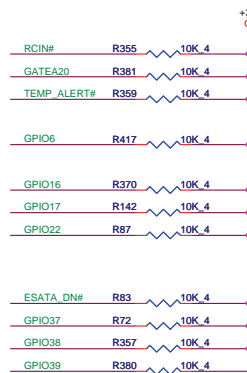
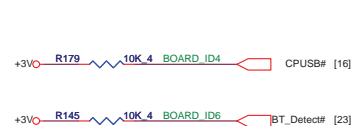
0 = Disables the VccVRM. Need to use on-board filter circuits for analog rails.
1 = Enables the internal VccVRM to have a clean supply for analog rails. No need to use on-board filter circuit. This signal has a weak internal pull-up.

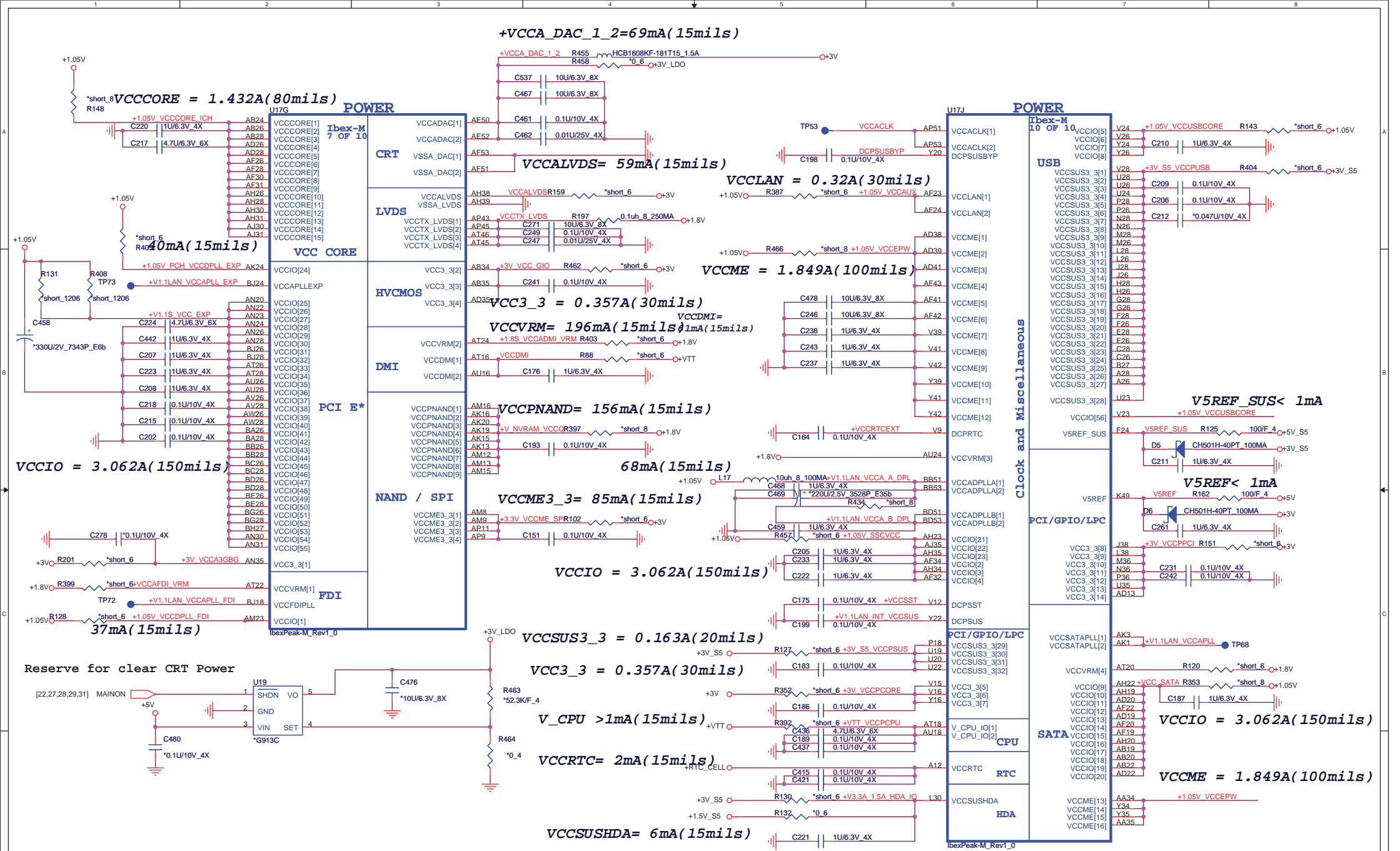
Quanta Computer Inc.
PROJECT : TE2

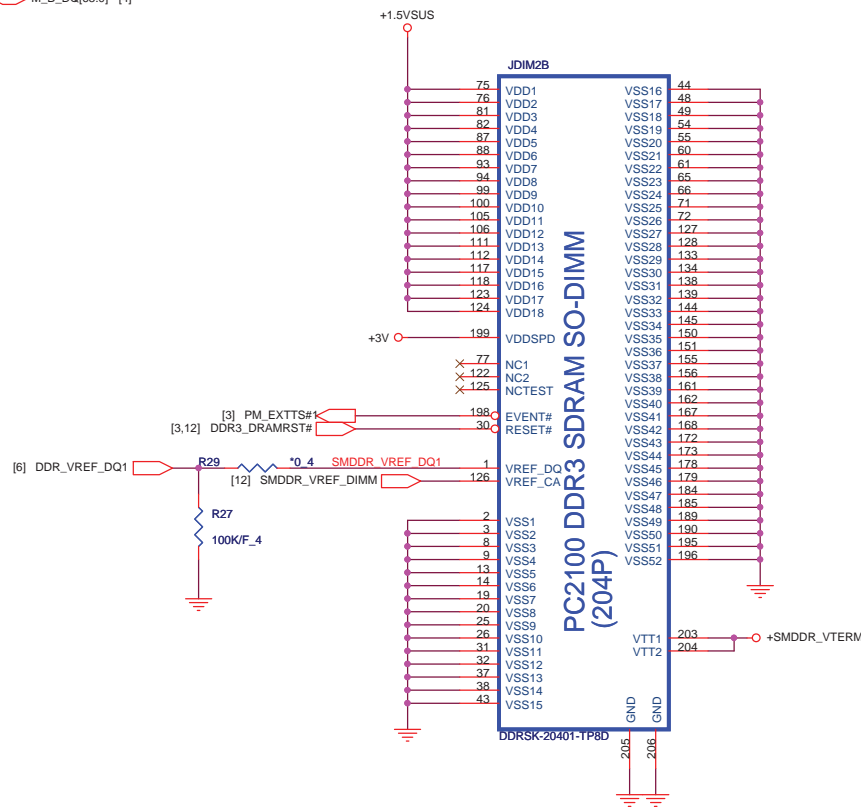
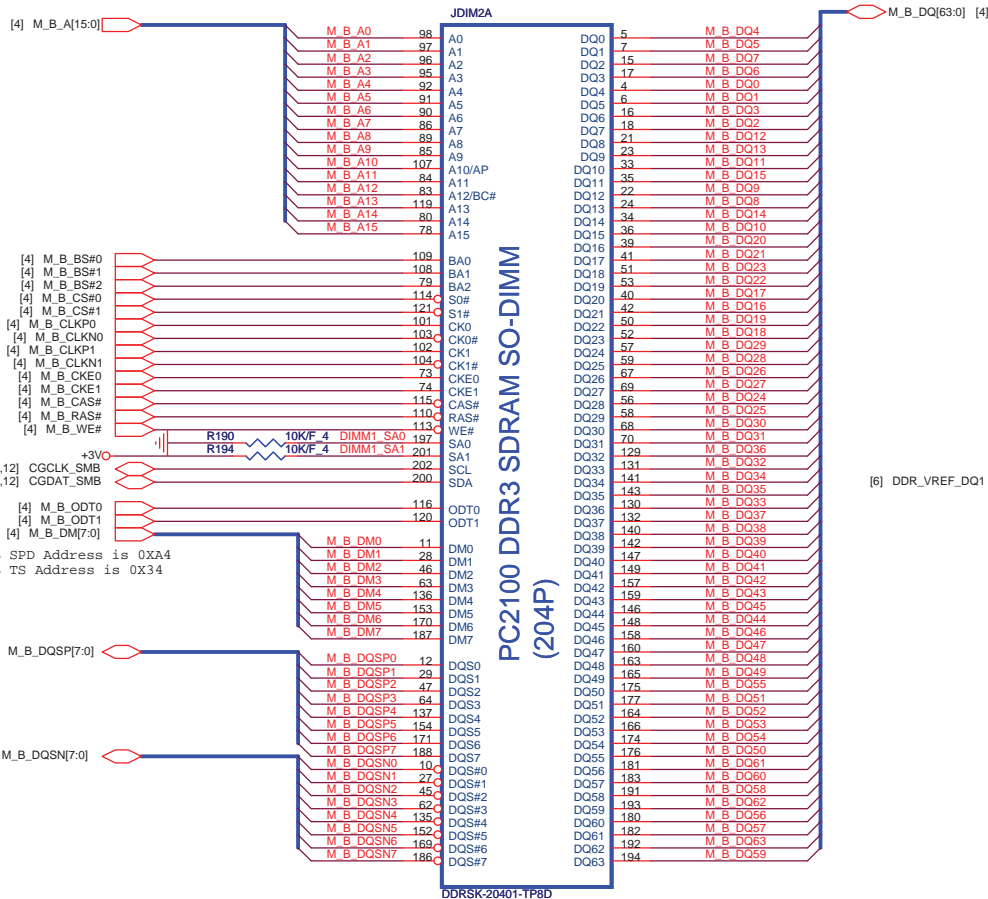
Size	Document Number	Rev
	PCH 4/5 (GPIO & Strap)	2A
Date:	Wednesday, March 10, 2010	Sheet 10 of 35

BOARD ID SETTING

Board ID	ID1	ID2	ID3	ID4	ID5	ID6
UMA SKU	H					
VGA SKU	L					
W/ MDC		H				
W/O MDC		L				
W/ HDMI			H			
W/O HDMI			L			
W/O 3G				H		
W/ 3G				L		
1.5"					H	
1.4"					L	

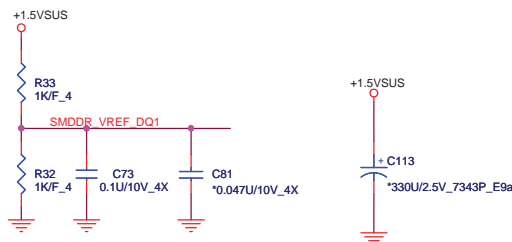
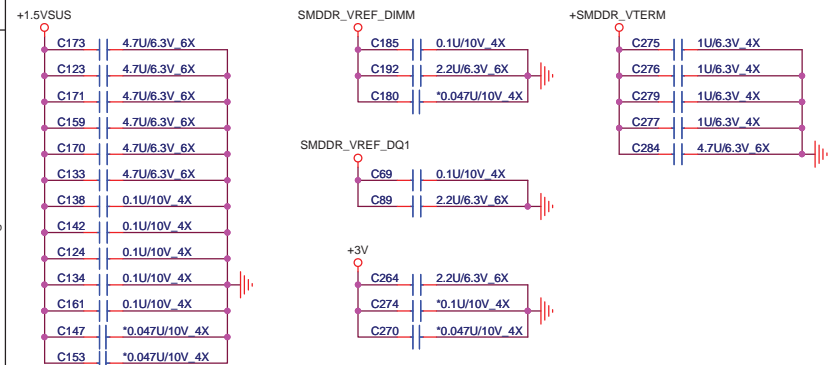






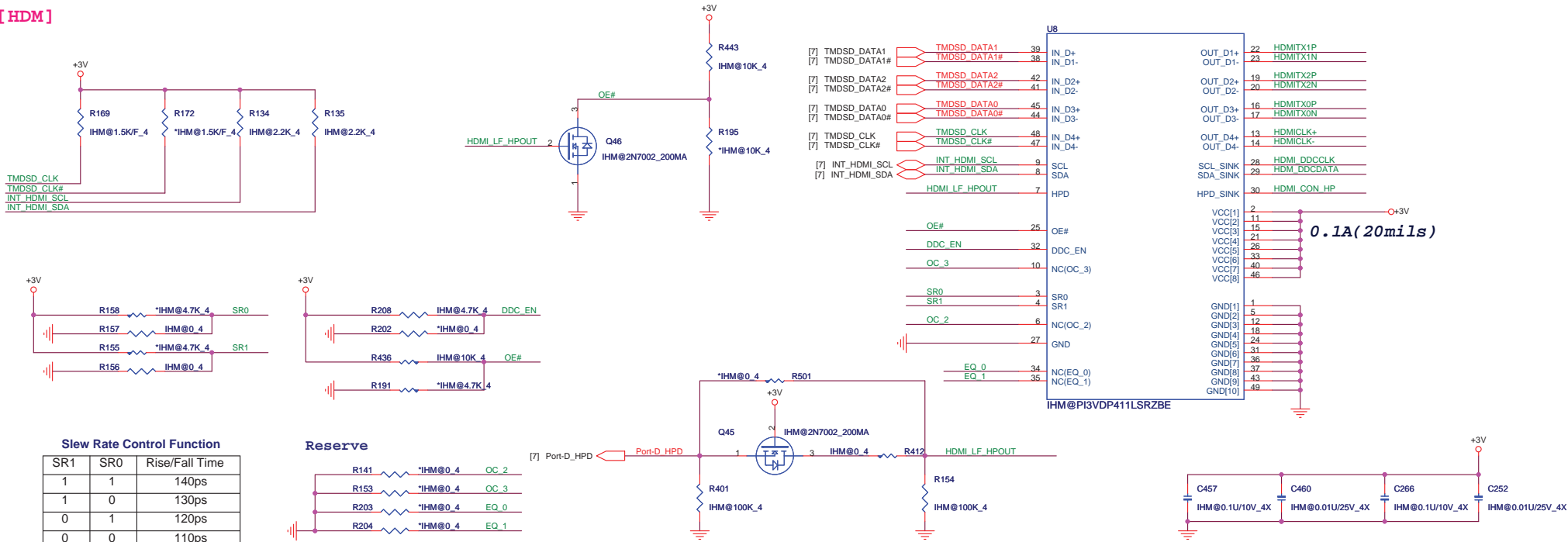
Place these Caps near So-Dimm1.

Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%

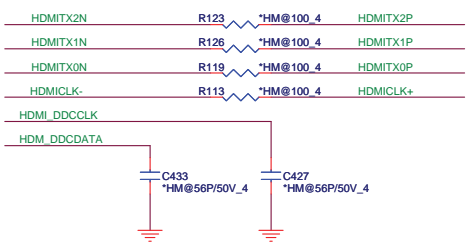


HDMI Conn
HDMI Level Shift UMA only

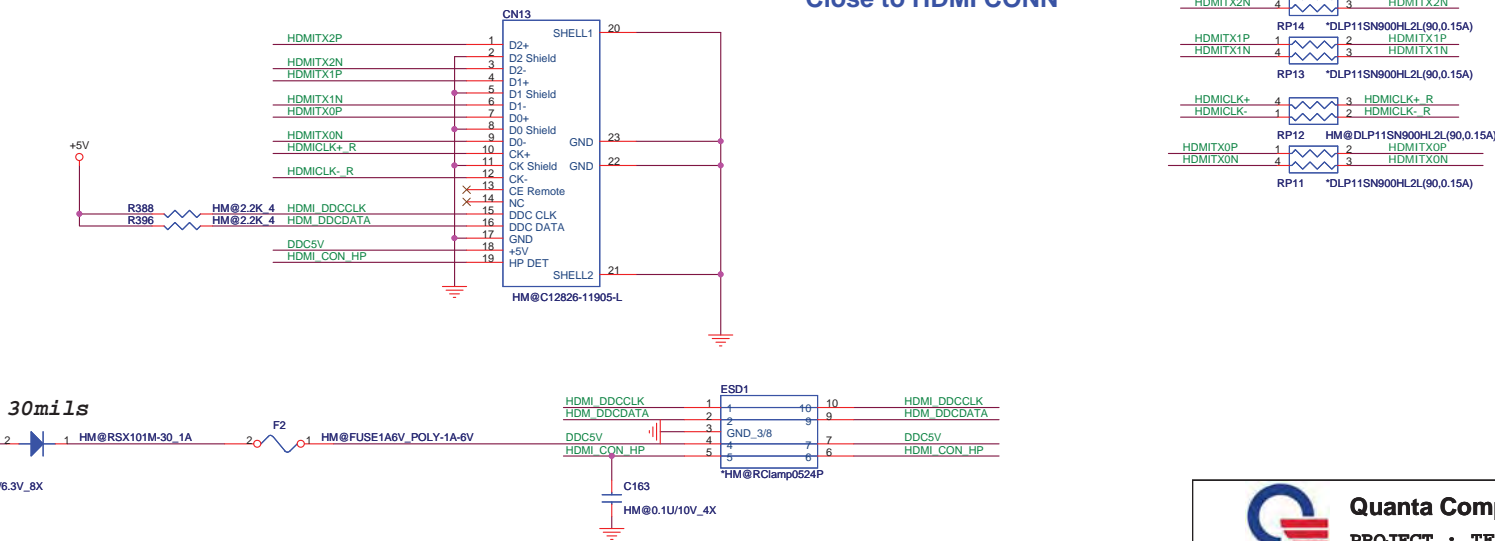
[HDM]



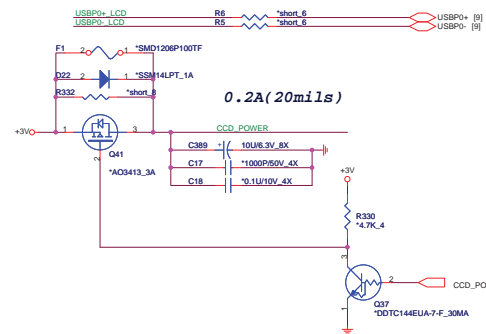
For EMI close to connector



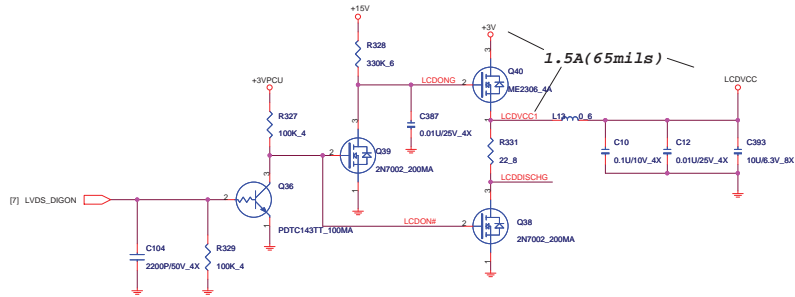
Close to HDMI CONN



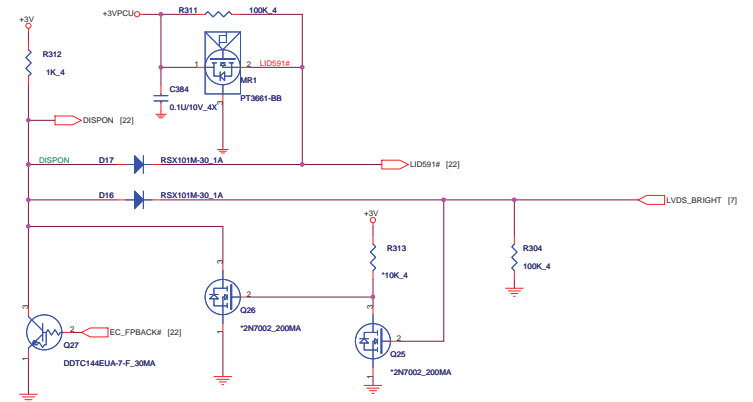
CCD [CCD]



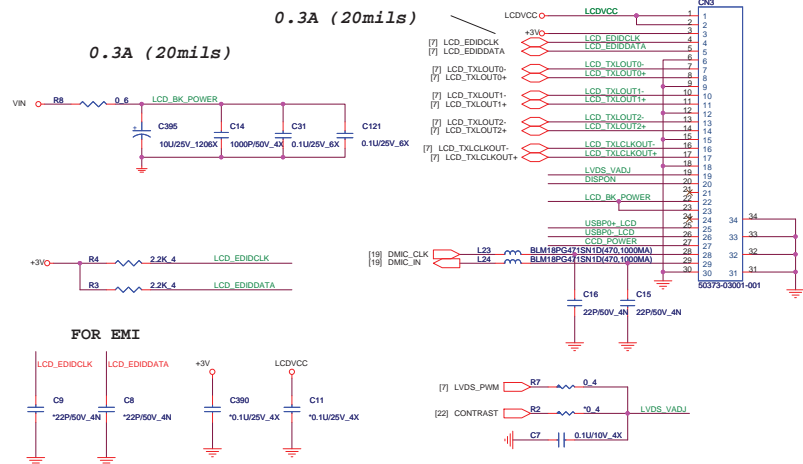
LCD POWER SWITCH [LDS]



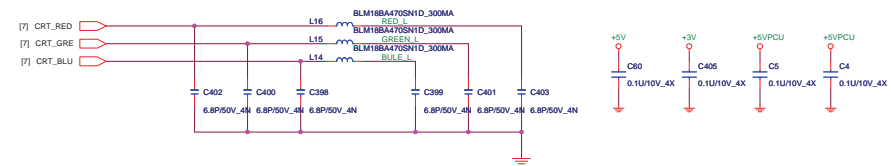
HALL SENSOR&BACK LIGHT SWITCH [HSR]



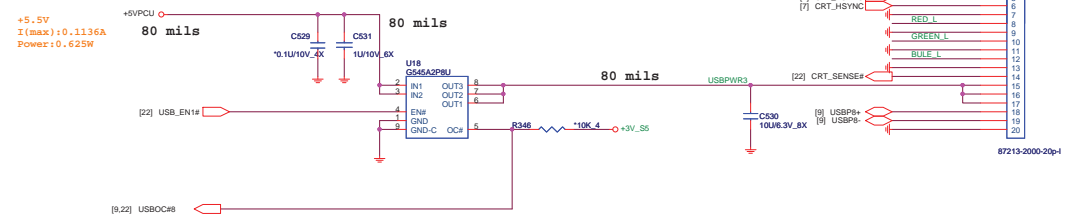
LCD Panel Module [LDS]



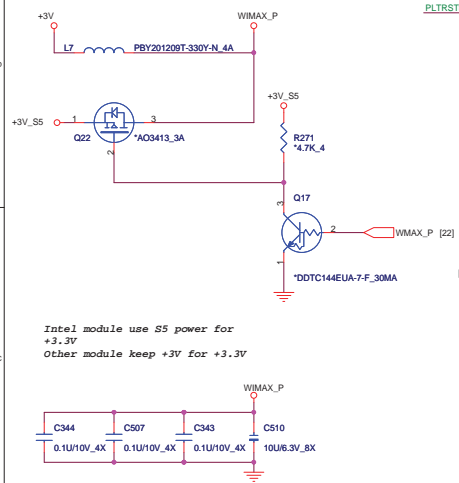
CRT [CRT]



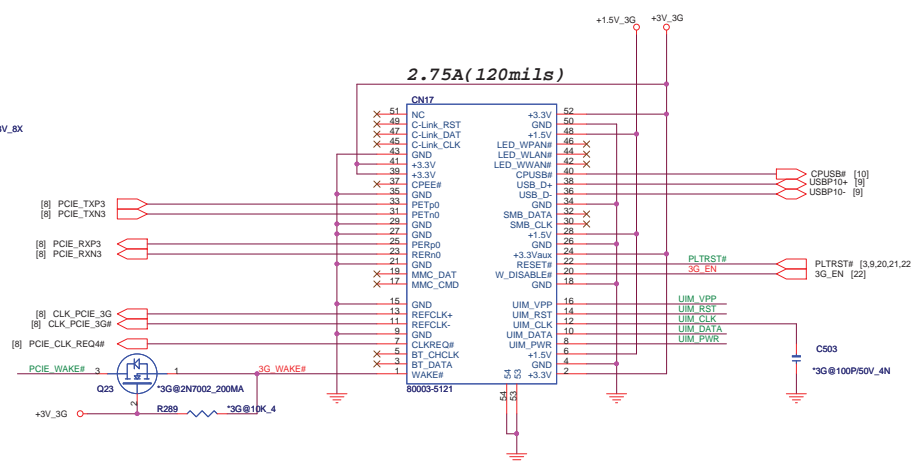
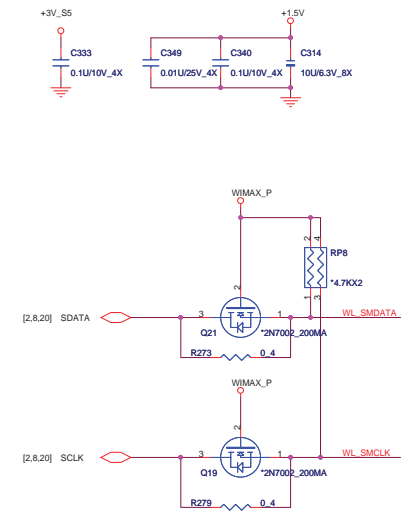
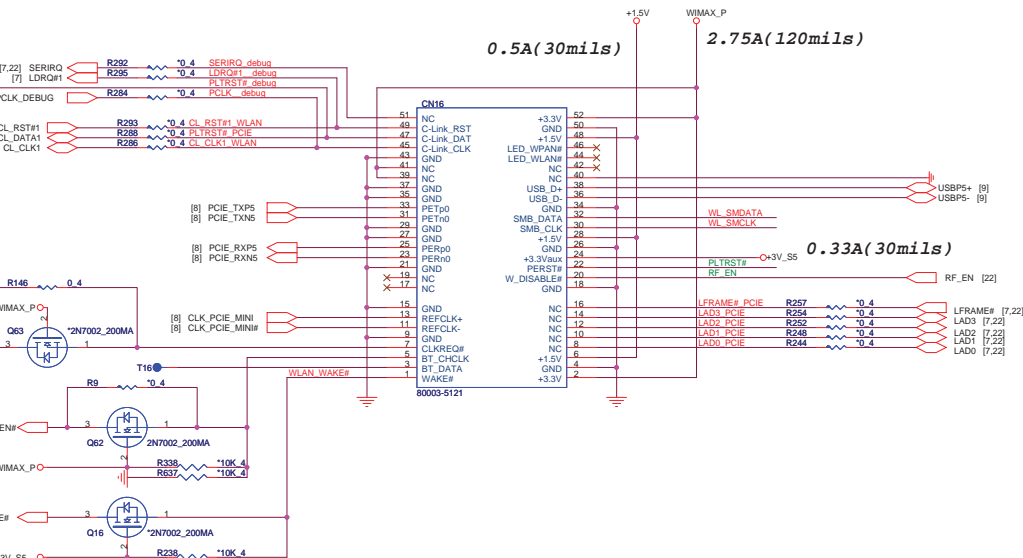
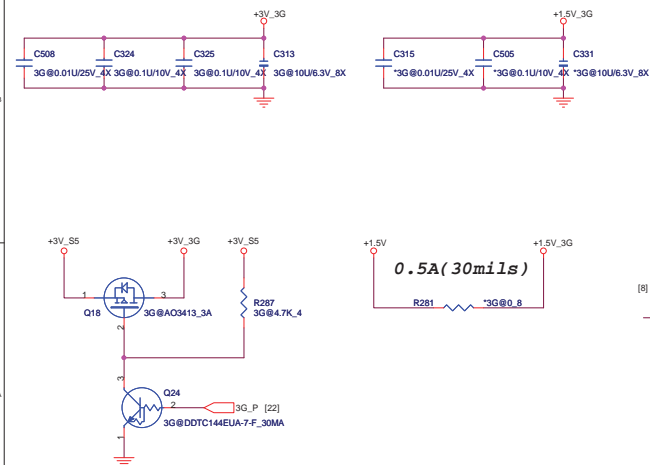
USB for CRT BOARD [USB]



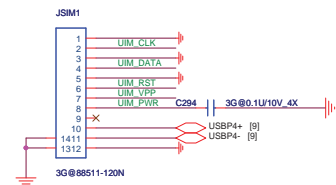
```
MINI Card Slot#1
(WiFi) [WLN]
```

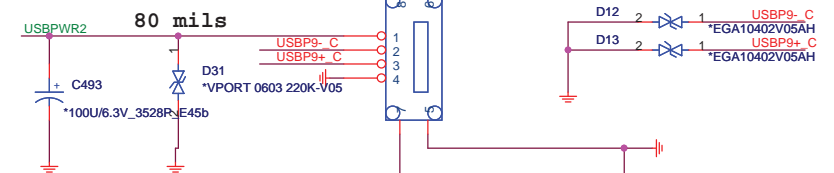


MINI Card Slot#2
3G [M3G]



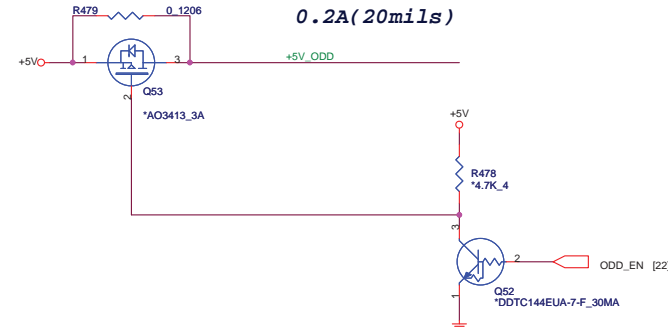
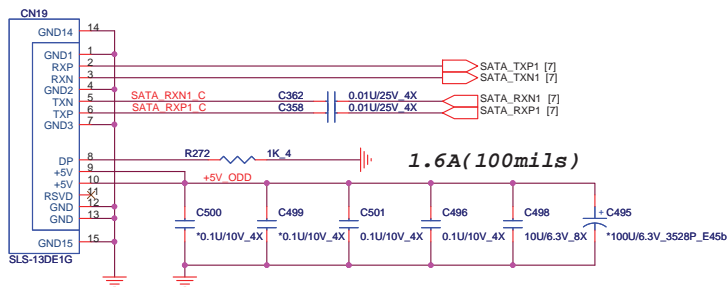
SIM CARD





SATA ODD

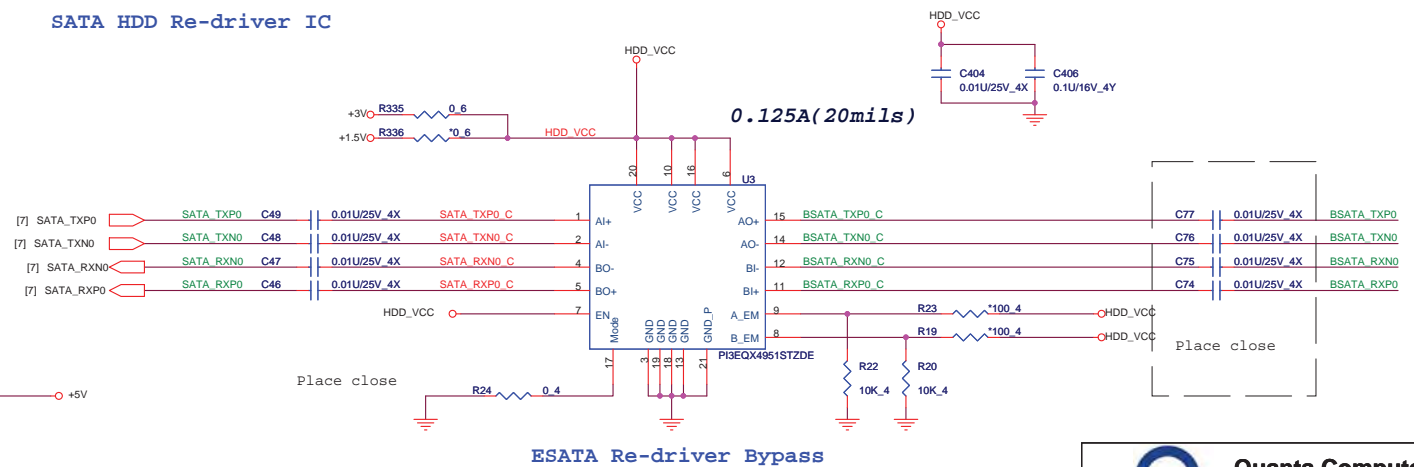
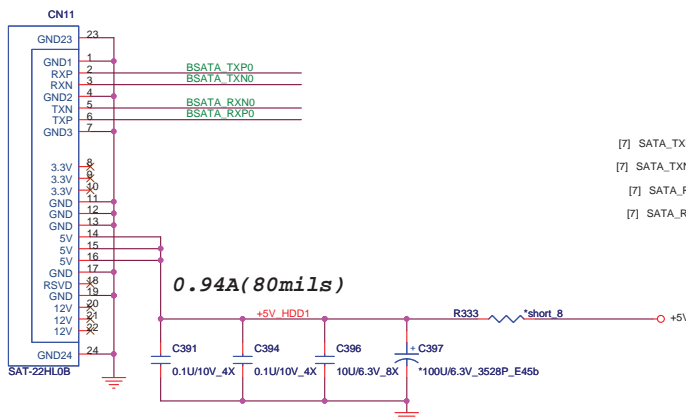
[ODD]




SATA HDD

[HDD]

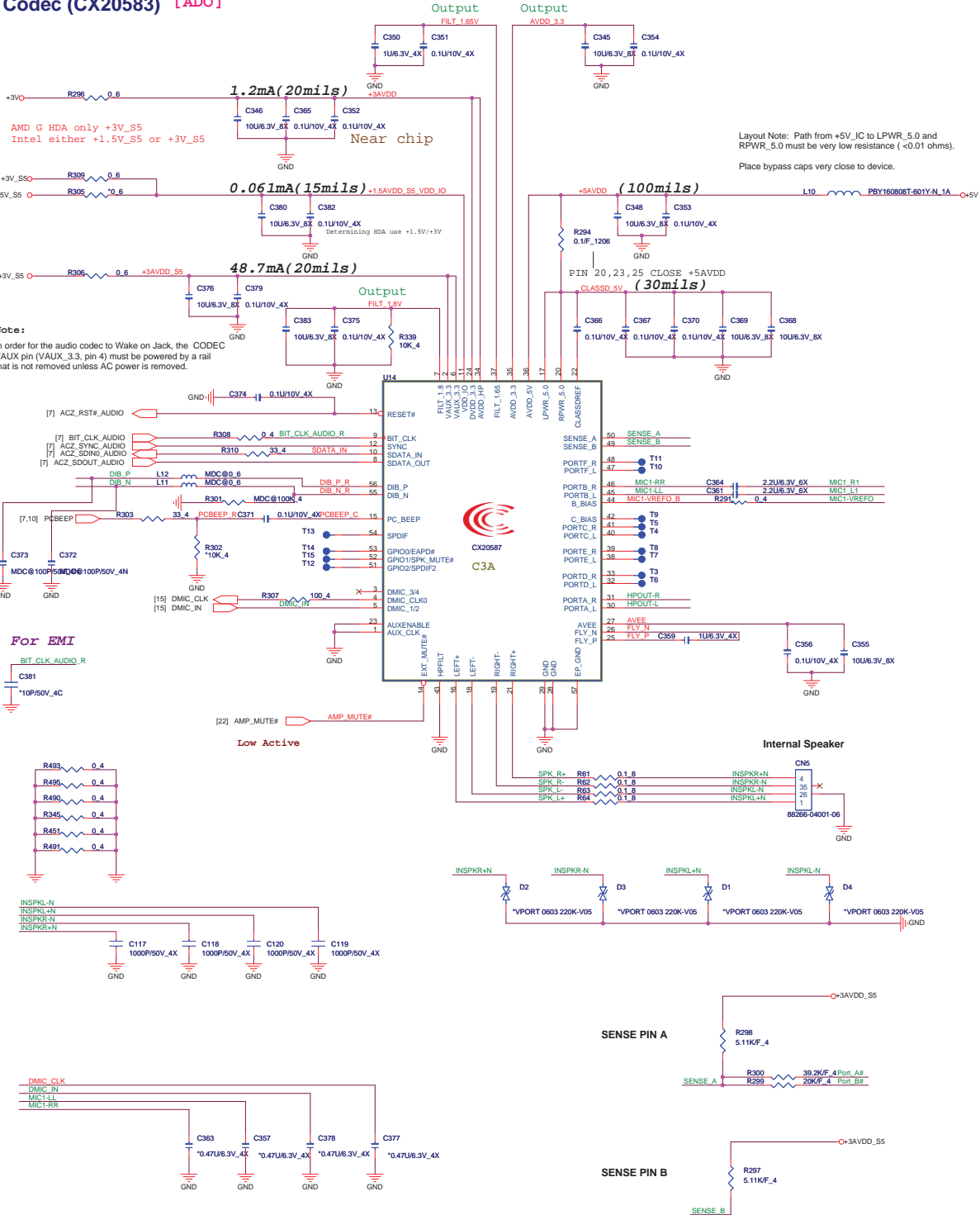
SATA HDD Re-driver IC



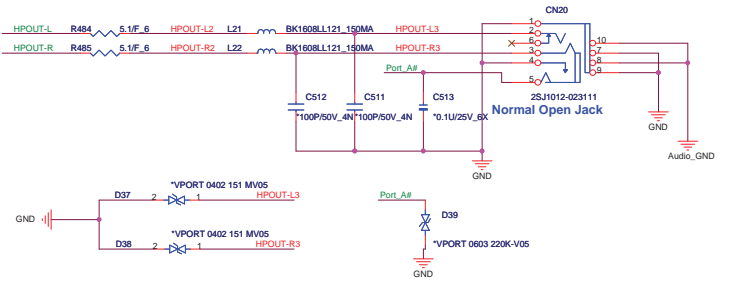
ESATA Re-driver Bypass

 Quanta Computer Inc. PROJECT : TE2		
Size	Document Number	Rev
	HDD/ODD/MDC	2A
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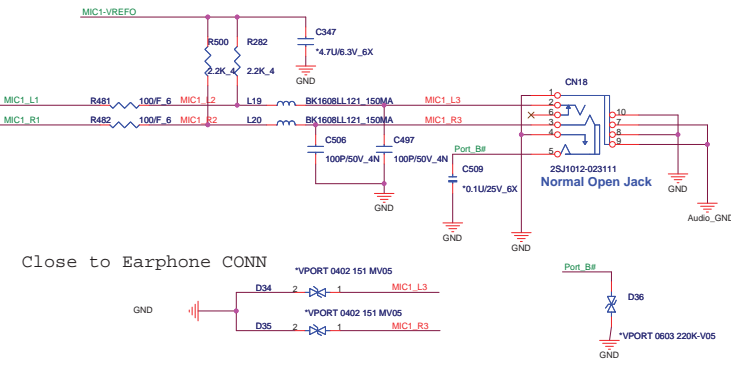
Codec (CX20583) [ADO]



Earphone



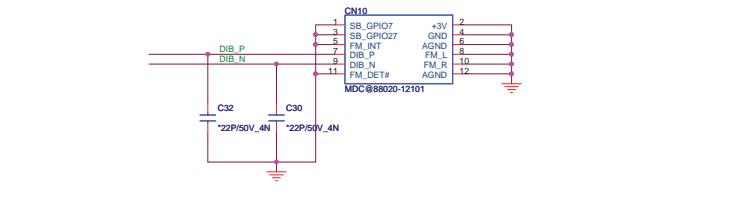
External MIC



Close to Earphone CONN

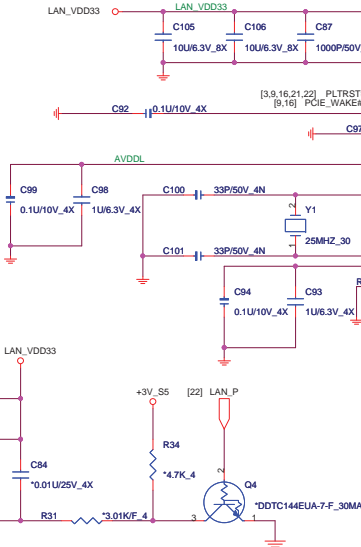
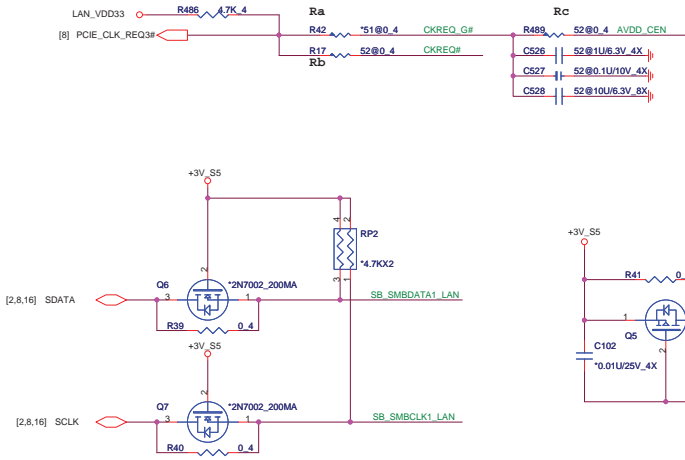


MDC



Atheros Lan

GIGA-STUFF
R_g
10/100-STUFF
R_g
LDO MODE :STUFF
R_c



D3A : Remove Q4 for cost issue

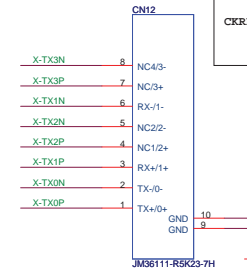
Atheros
AR8151/AR8152

GIGA:AR8151-AL1A-R
= AL008151001

10/100:AR8152-AL1A-R
= AL008152004

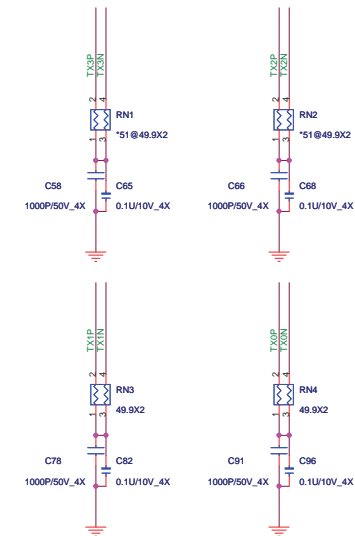
D3A : CN12 change footprint for BMT open issue.

RJ45

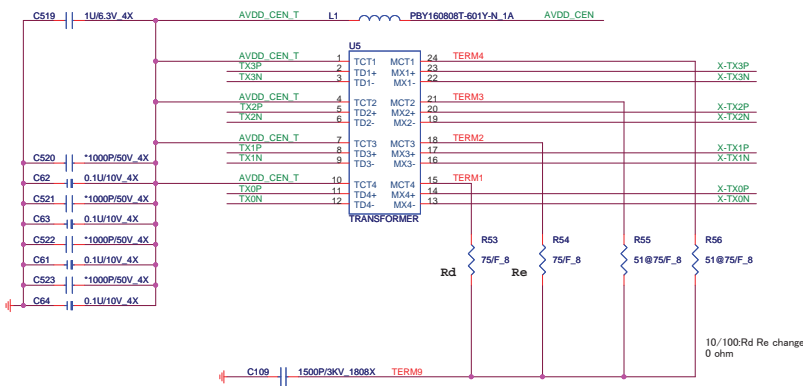


LED0 = LAN_ACTLED	1	Over-clocking enable (default = 1)
	0	Over-clocking disable
LED1 = LAN_LINKLED#	1	SWR switch-mode regulator select Giga LAN pull High (default = 1)
	0	LDO linear regulator select 10/100M LAN pull Low
CKREQ# or CKREQ_G#	1	Normal function
	0	ATE test mode

PLACE NEAR LAN IC SIDE



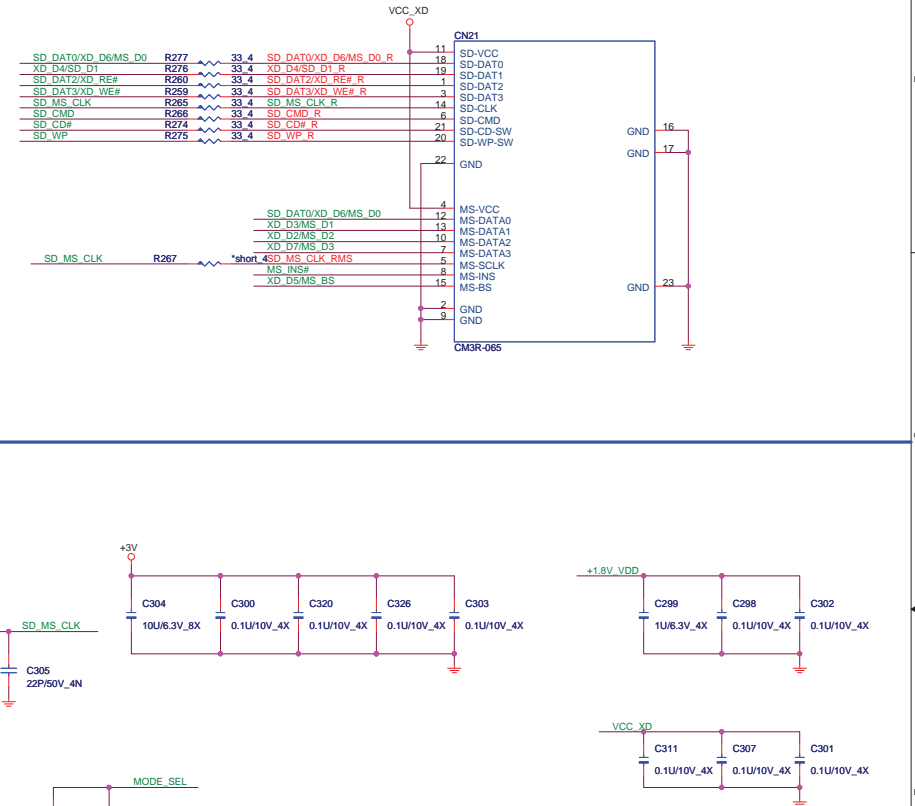
TRANSFORMER




WWW.AliSaler.Com



	R49	C73	Power mode
RTS 5159	0-ohm	NC	USB Auto De-link mode:



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	RTS5159 (Card Reader)	2A
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I/O Base Address

I/O Address	
BADDR1-0	Data
0 0	XOR TREE TEST MOD
0 1	CORE DEFINED
1 0	2Eh 2Fh
1 1	164Eh 164Fh

SHBM=0: Enable shared memory with host BIOS



Disabled ('1') if using FWH device on LPC.
Enabled ('0') if using SPI flash for both system BIOS and EC firmware

ID



SPI FLASH

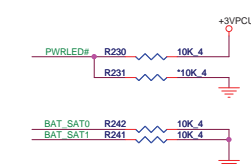


Intel	512KB	W25X40BVSSIG
AMD	2MB	W25Q16BVSSIG

INTERNAL KEYBOARD STRIP SET



HWPG



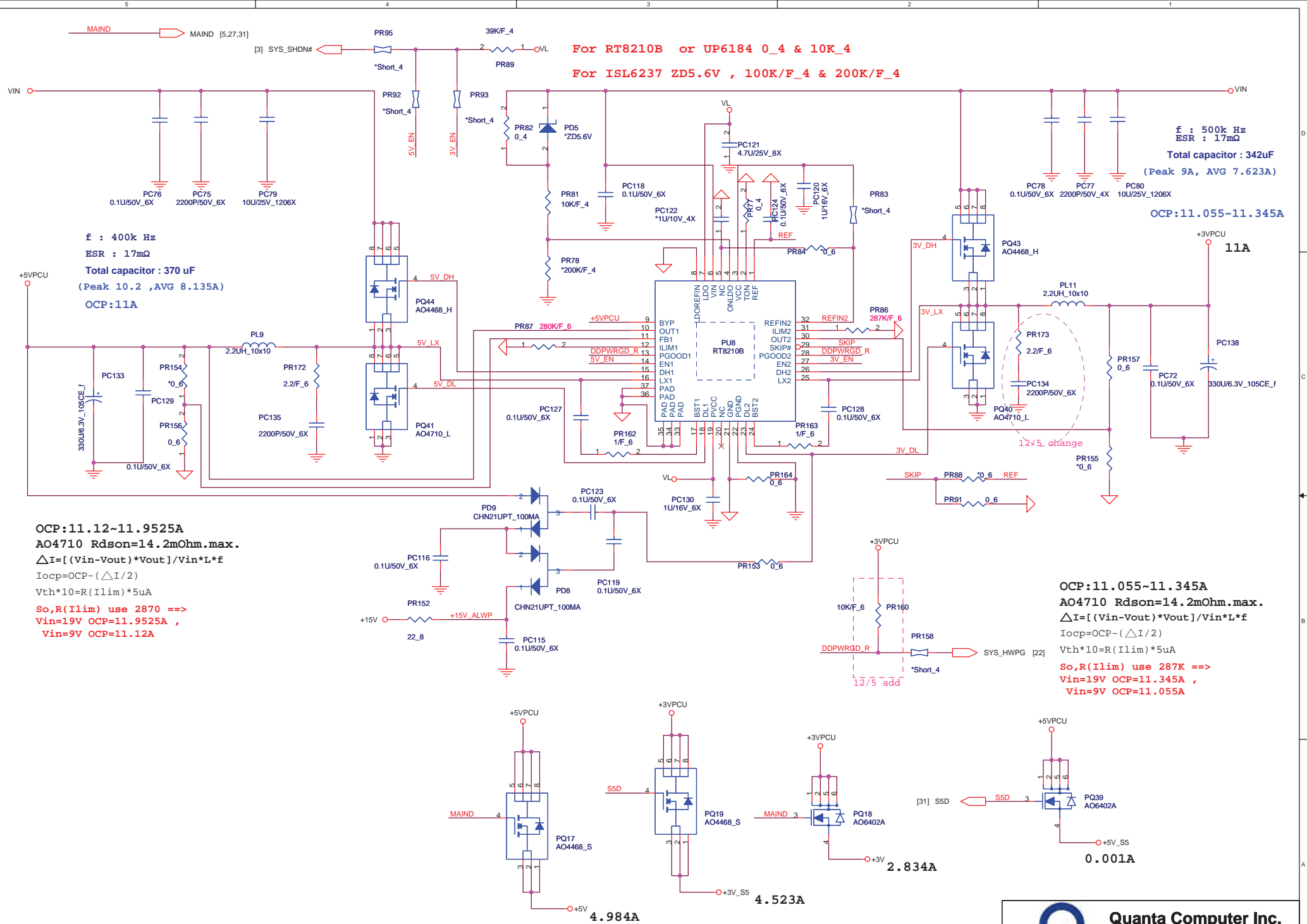
SMBUS Table

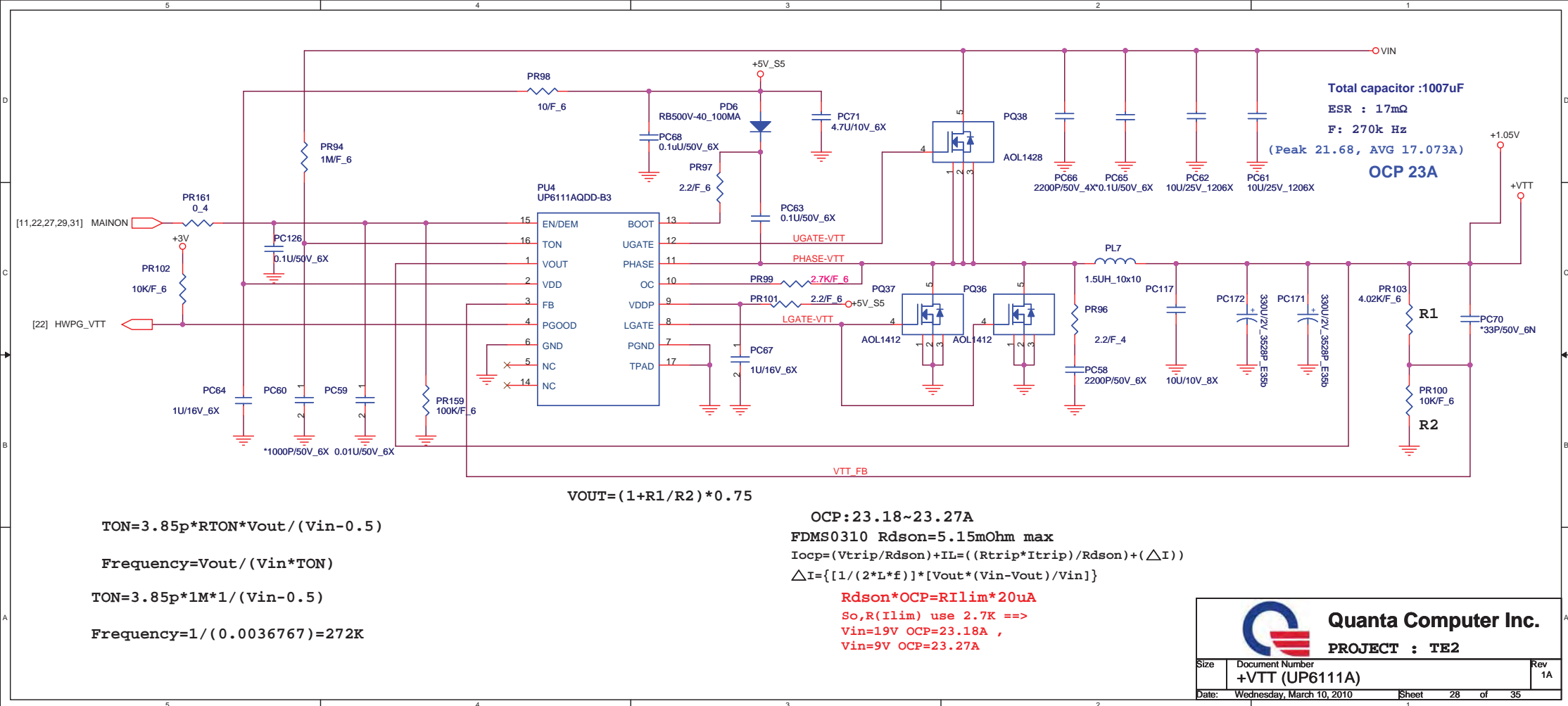
SMBUS	Devices	Address
1	Battery	12H
2	CPU Board Thermal Sensor	
	EC EEPROM	A0H
3	VGA Board Thermal Sensor	98H




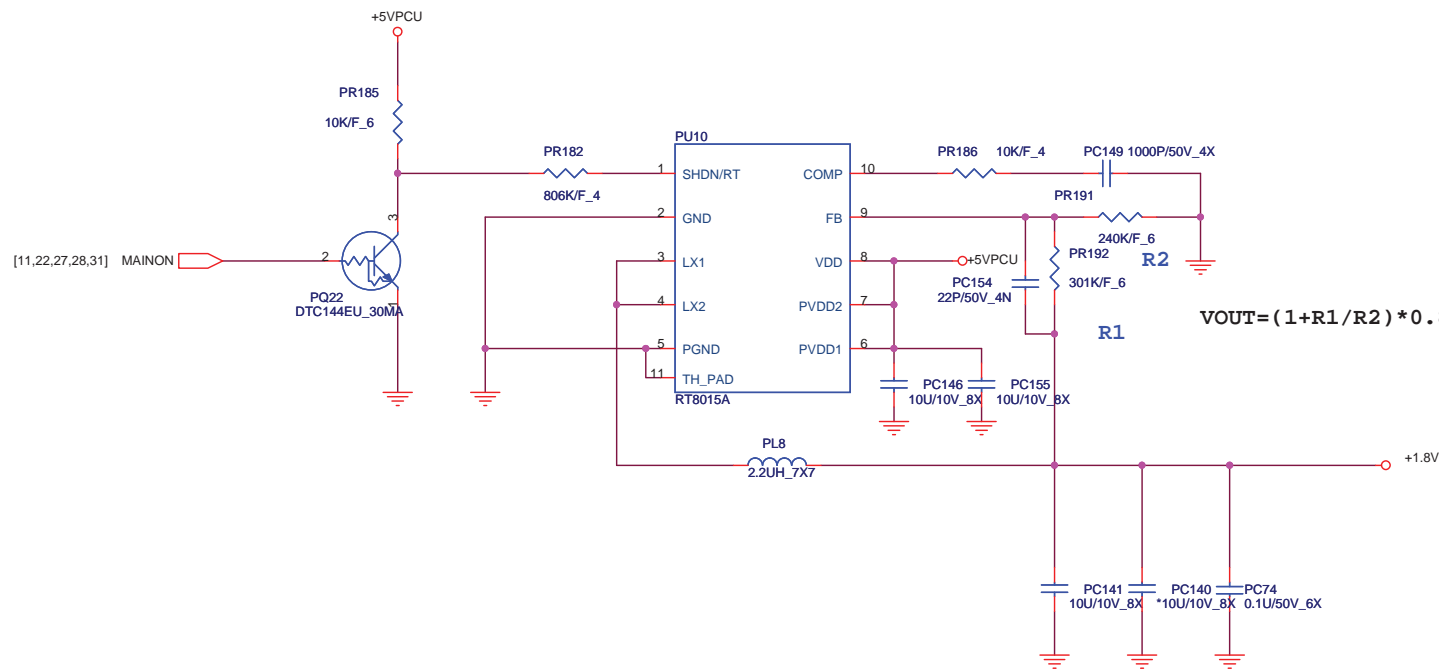
D3A : Add R240 pull high to 10K







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OCP Fellow IC spec~3.7A

$$VOUT = (1 + R1/R2) * 0.8$$

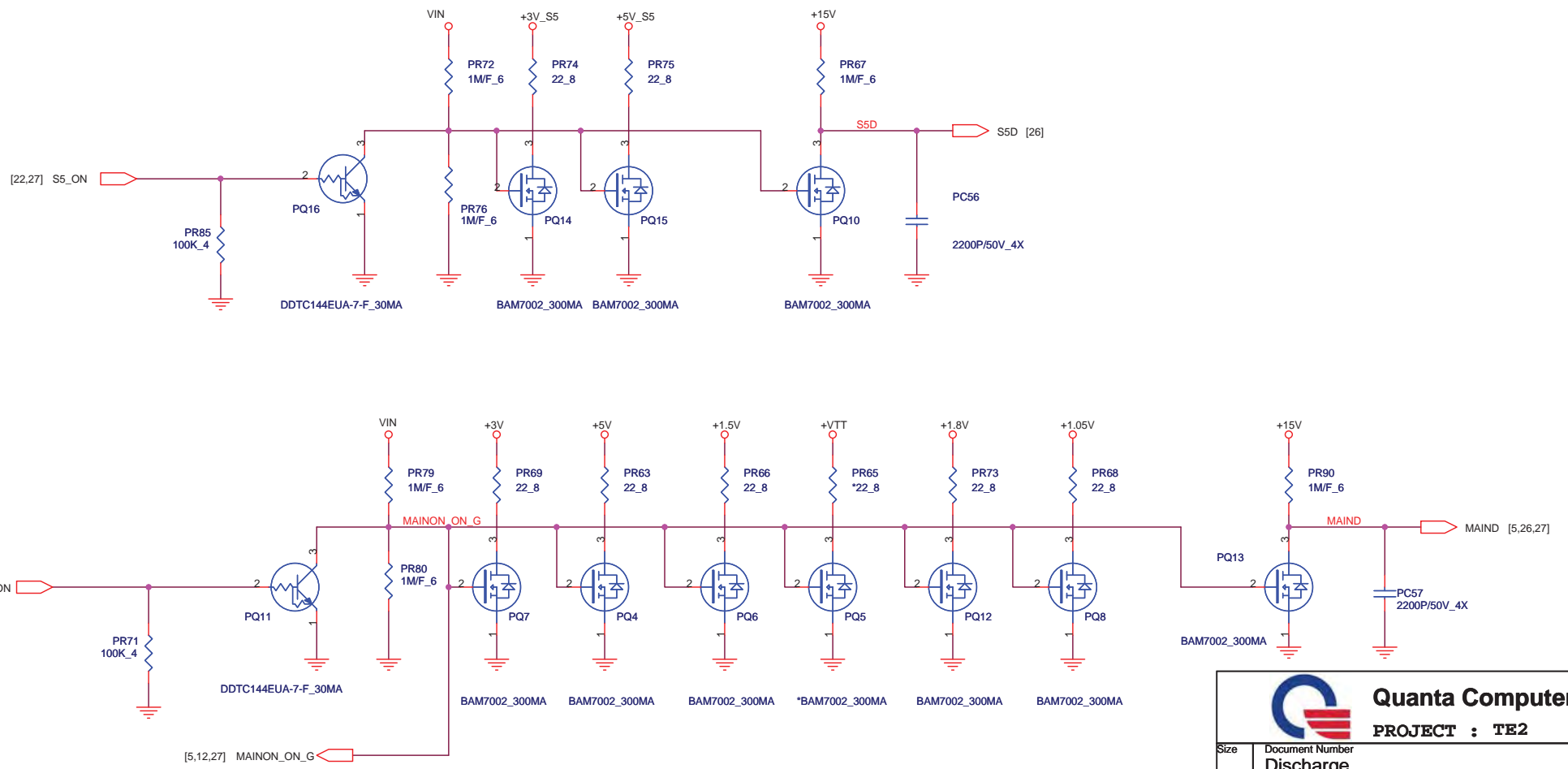
FOR UMA 0.194A
For VGA 1.345A




Quanta Computer Inc.

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	+1.8V (RT8015A)	1A
Date:	Tuesday, March 09, 2010	Sheet 29 of 35



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Size	Document Number	Rev
	Discharge	1A
Date:	Wednesday, March 10, 2010	Sheet 31 of 35

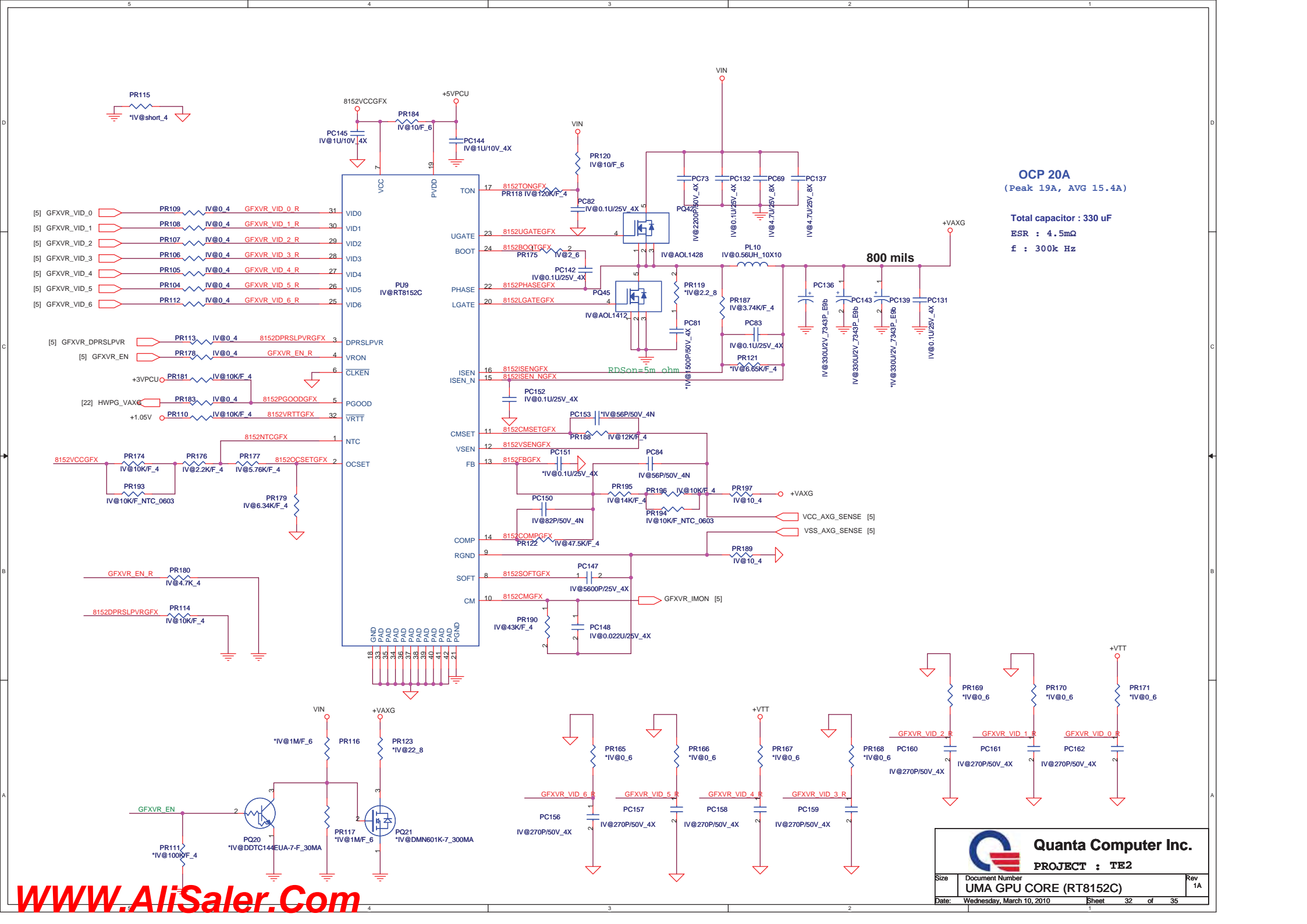


Table of Contents

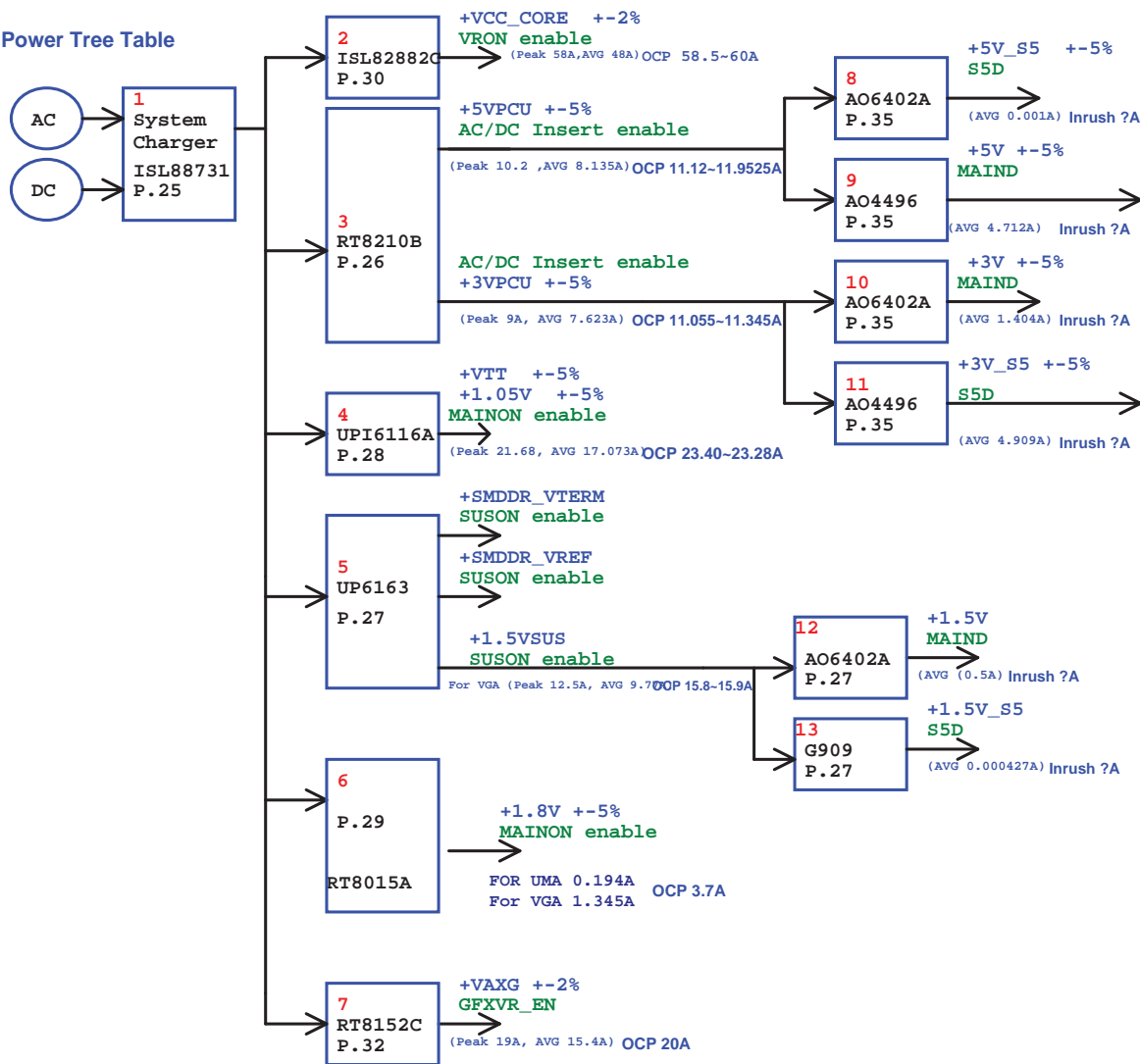
PAGE	DESCRIPTION	BOI-FUNCTIONS
1	Schematic Block Diagram	
2	Front Page	
3	Clock Generator	CLK
4-7	Processor	CPU
8-14	PCH	CLG
9	RTC	RTC
15-16	DDRIII SO-DIMM	DDR
17	VGA Connector	VGA
18	LCD Panel	LDS
	CRT & CRT BUS SWITCH	CRT
	CCD	CCD
	HALL SENSOR&BACK LIGHT SWITCH	HSR
19	Display Port	DPP
20	HDMI comm part	HDM
	HDMI for GM	HMG
21	SATA ODD	ODD
	Main SATA HDD & 2nd SATA HDD	HDD
	G-Sensor	H3D
22	5 IN 1 Card reader	MMC
	IEEE1394	FIW
23	MINI Card (Wi-Fi & WIMAX)	WLN
	MINI Card 2nd	MNC
	MINI Card 3rd	MNC
	TMA Connector	TMA
24	INT KeyBoard & K/B LED Power	KBC
	LED Board	LED
	TP&FP board	TPD,FPD
	Bluetooth Connector	BTM
	Felica Connector	FEC
	MMB Connector	MMB
	Power SW	PSW
	B-CAS Connector	BCS
25	New Card (Express Card)	EXC
	E-SATA comb USB	ESA
	USB Connector	USB
	Audio & USB Board	USB,ADO
	Light Sensor	LSN
	Satellite LED	LED
	RF LED / WIMAX LED / Kill SW	KSW
26	EC WP8763LDG/WPC8769L(O)	KBC
	CIR	CIR
27	Codec (CX20583)	ADO
28	FM Tunner	FMM
	Modem Connector	MDM
	HOLE	
29	Atheros LAN	LAN
30	NVRAM Connecytor	NVR
31	Charger (ISL6251A)	PWM
32	System 5V/3V (ISL6237)	PWM
33	CPU CORE (ISL62882)	PWM

POWER PLANE	VOLTAGE	CONTROL SIGNAL	Power States ACTIVE IN
VIN	10V~+19V		S0~S5
+VCCRTC	+3.0V~+3.3V		S0~S5
+3V	+3.3V	MAIN_ON	S0
+3V_S5	+3.3V	S5_ON	S0~S5
+3V_HDP	+3.3V	MAIN_ON	S0
+3VPCU	+3.3V	AC/DC Insert enable	S0
+5V	+5V	MAIN_ON	S0
+5V_S5	+5V	S5_ON	S0~S5
+5VPCU	+5V	AC/DC Insert enable	S0~S5
+5V_TMA	+5V	MAIN_ON	S0
WIMAX_P	+3.3V	WMAX_P for EC	
+1.8V	+1.8V	MAIN_ON	S0
+1.5V	+1.5V	MAIN_ON	S0
+1.5V_S5	+1.5V	S5_ON	S0~S5
+1.5V_SUS	+1.5V	SUSON	S0~S3
+VCC_CORE		VRON	S0
+VTT	+1.05V~+1.1V	MAIN_ON	S0
+1.05V	+1.05V	MAIN_ON	S0
+VAXG		GFXVR_EN	S0

GND PLANE	PAGE
⏚ GND_SIGNAL	32
⏚ CARD_GND	21
⏚ AGND_DC/DC	31
⏚ GND	ALL

PAGE	DESCRIPTION	BOI-FUNCTIONS
34	VAXG (ISL62881)	PWM
35	+VTT (UP6111A)	PWM
36	+1.05V (UP6111AQDD)	PWM
37	DDR 1.5V (TPS51116)	PWM
38	Discharge (1.5V_S5/1.8V)	PWM
39	Power Tree Table	
40	PCH Power Plane	
41	Power Management	
42	Change List	

Power Tree Table



Power Distribution List

Power	Distribution

Model		CHANGE LIST					MODEL			TE2		
		REV						PAGE	FROM	To		
TE2 MB	B2A	PAGE(16) : Add BT_EN# for combo RF control for BT					1	1A				
		PAGE(27) : Change DDR S3 1.5V ON circuit.					2	1A				
	C3A						3	1A				
		PAGE(07) : Add ESATA re-driver IC					4	1A				
	D3A						5	1A				
		PAGE(24) : LED luminance to light,R321、R319 1K-ohm change 2.2K-ohm.					6	1A				
		PAGE(24) : LED luminance too low,R317 560-ohm change 220-ohm.					7	1A				
		PAGE(19) : Add R61,R62,R63,R64 0.1-ohm to avoid speaker burn.					8	1A				
		PAGE(16) : Add Q62 to avoid leakage current.					9	1A				
							10	1A				
							11	1A				
							12	1A				
							13	1A				
							14	1A				
							15	1A				
							16	1A				
							17	1A				
							18	1A				
							19	1A				
							20	1A				
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							27	1A				
							28	1A				
							29	1A				
							30	1A				
DOC NO. 204		PROJECT MODEL :	TE2	APPROVED BY:	Mosy Li	DATE:	2009/11/13	<div><div><div></div><div></div></div><div>Quanta Computer Inc.</div><div>PROJECT : TE2</div><div>Change list</div><div>Friday, March 19, 2010</div><div>Sheet 31 of 35</div></div>				
		PART NUMBER:		DRAWING BY:	Mosy Li	REVISION:	1A					